

TM80N04DF

N-Channel Enhancement Mosfet

General Description

- Low $R_{DS(ON)}$
- RoHS and Halogen-Free Compliant

Applications

- Load switch
- PWM

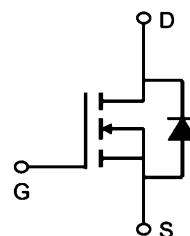
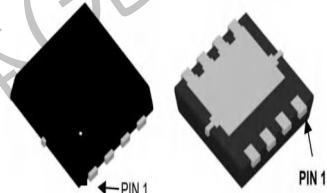
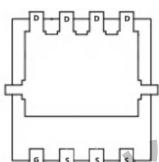
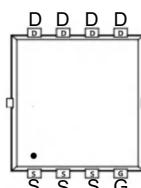
General Features

$V_{DS} = 40V$ $I_D = 80A$
 $R_{DS(ON)} = 4.2m\Omega$ (typ.) @ $V_{GS} = 10V$

100% UIS Tested
 100% R_g Tested



DF:PDFN3x3-8L



Marking: 80N04

Absolute Maximum Ratings ($T_c=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	40	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_c = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	80	A
$I_D @ T_c = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	44.3	A
I_{DM}	Pulsed Drain Current	280	A
E_{AS}	Single pulsed avalanche energy	100	mJ
$P_D @ T_c = 25^\circ C$	Total Power Dissipation ⁴	52	W
T_{STG}	Storage Temperature Range	-55 to 175	°C
T_J	Operating Junction Temperature Range	-55 to 175	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	---	62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case	---	2.4	°C/W

TM80N04DF
N-Channel Enhancement Mosfet

 Electrical Characteristics: ($T_c=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250 \mu\text{A}$	40	---	---	V
$I_{\text{DS}}^{\text{SS}}$	Zero Gate Voltage Drain Current	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=40\text{V}$	---	---	1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{A}$	---	---	± 100	nA
On Characteristics						
$V_{\text{GS}(\text{th})}$	Gate-Source Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=250 \mu\text{A}$	1.2	1.6	2.0	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On Resistance ³	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=20\text{A}$	---	4.2	5.5	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=10\text{A}$	---	5.5	7	
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	---	2410	---	pF
C_{oss}	Output Capacitance		---	233	--	
C_{rss}	Reverse Transfer Capacitance		---	152	---	
Switching Characteristics						
$t_{\text{d}(\text{on})}$	Turn-On Delay Time	$V_{\text{DS}}=20\text{V}, I_{\text{D}}=1\text{A}, R_{\text{G}}=3.3 \Omega, V_{\text{GS}}=10\text{V}$	---	14.2	---	ns
t_r	Rise Time		---	18.3	---	ns
$t_{\text{d}(\text{off})}$	Turn-Off Delay Time		---	38.8	---	ns
t_f	Fall Time		---	13.9	---	ns
Q_g	Total Gate Charge	$V_{\text{GS}}=4.5\text{V}, V_{\text{DS}}=32\text{V}, I_{\text{D}}=10\text{A}$	---	25	---	nC
Q_{gs}	Gate-Source Charge		---	6.4	---	nC
Q_{gd}	Gate-Drain "Miller" Charge		---	12.1	---	nC
Drain-Source Diode Characteristics						
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{SD}}=1\text{A}$	---	---	1	V
I_s	Continuous Drain Current	$V_D=V_G=0\text{V}$	---	---	80	A
I_{SM}	Pulsed Drain Current		---	---	280	A

TM80N04DF

N-Channel Enhancement Mosfet

Typical Characteristics: ($T_c=25^\circ\text{C}$ unless otherwise noted)

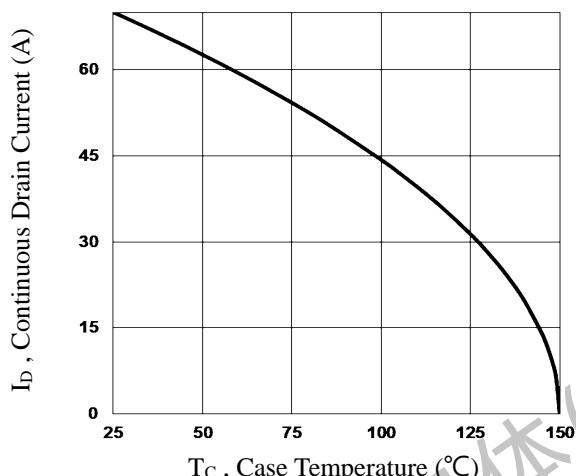


Fig.1 Continuous Drain Current vs. T_c

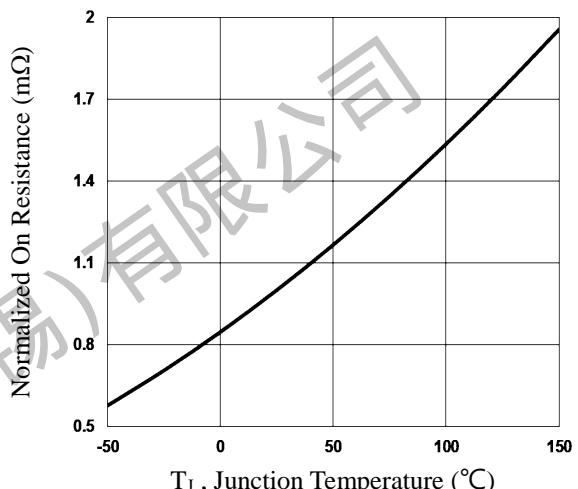


Fig.2 Normalized $R_{DS(on)}$ vs. T_j

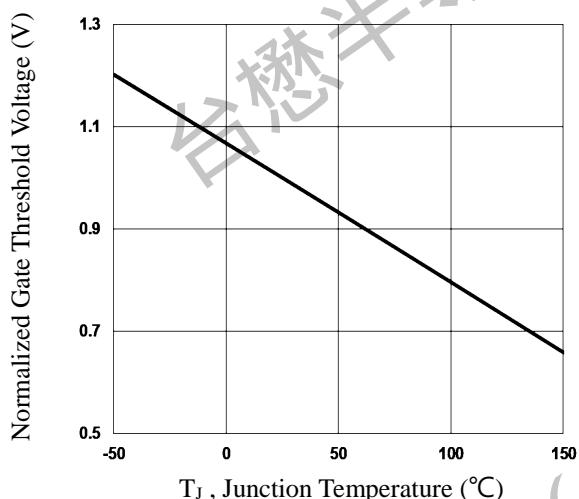


Fig.3 Normalized V_{th} vs. T_j

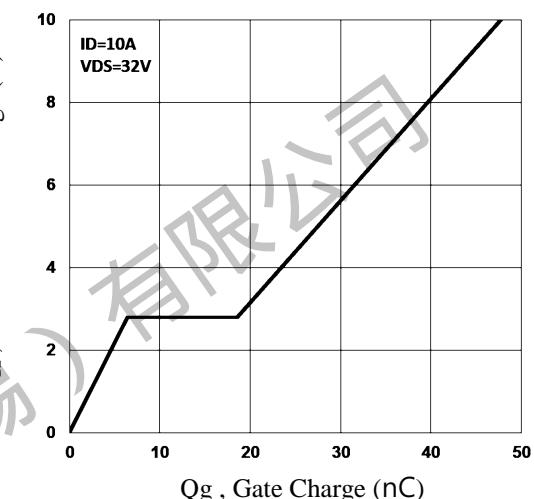


Fig.4 Gate Charge Waveform

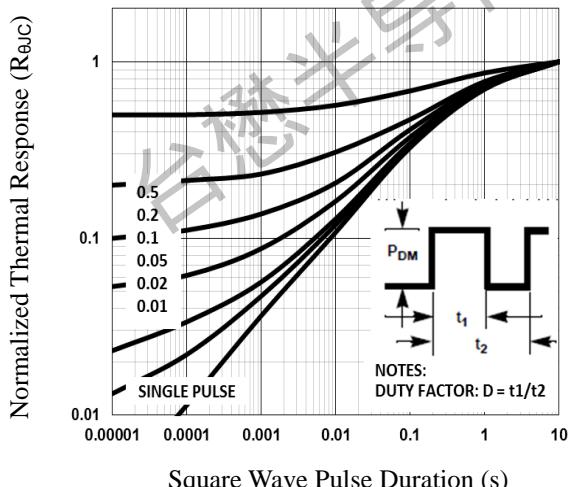


Fig.5 Normalized Transient Impedance

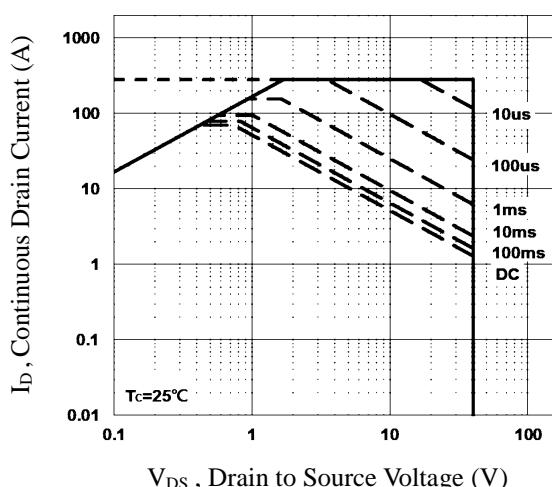


Fig.6 Maximum Safe Operation Area

TM80N04DF

N-Channel Enhancement Mosfet

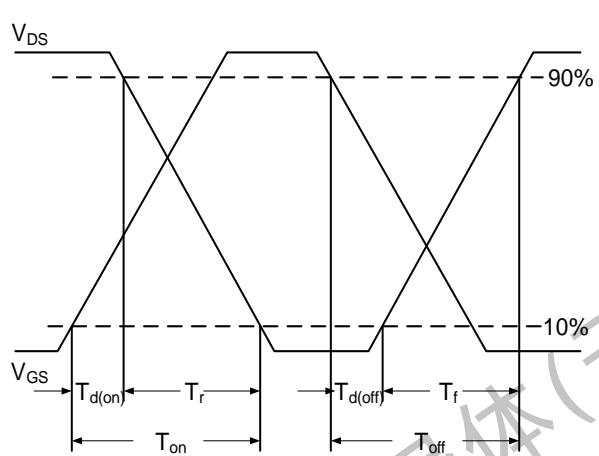


Fig.7 Switching Time Waveform

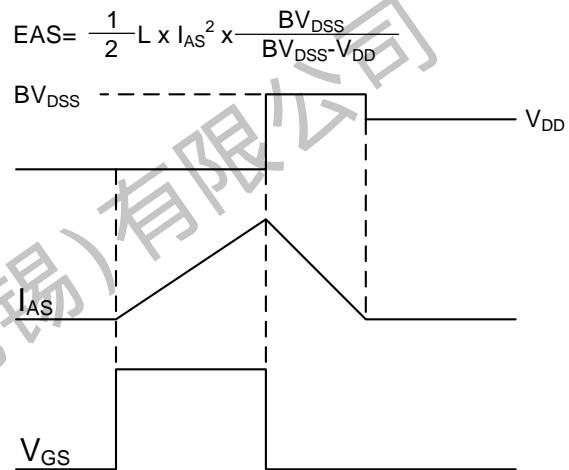
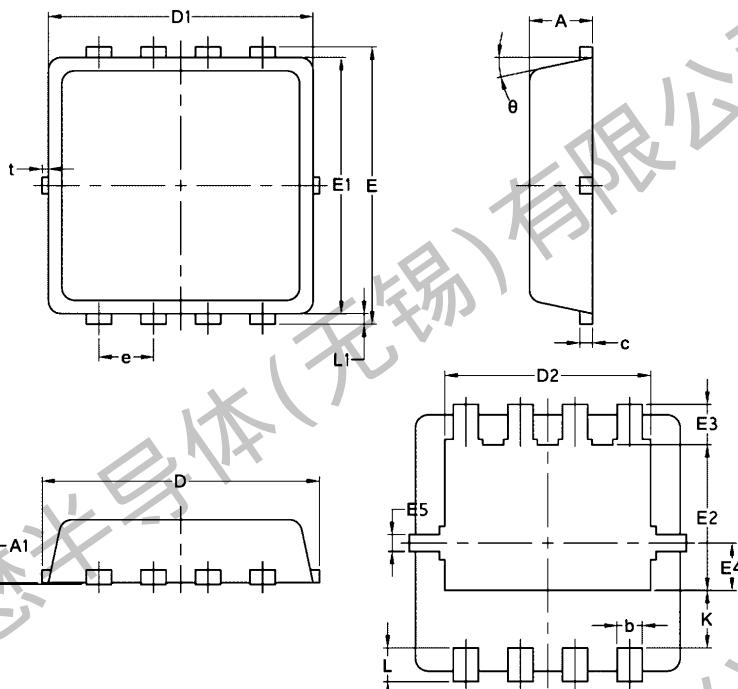


Fig.8 EAS Waveform

TM80N04DF

N-Channel Enhancement Mosfet

Package Mechanical Data: DFN3x3-8L

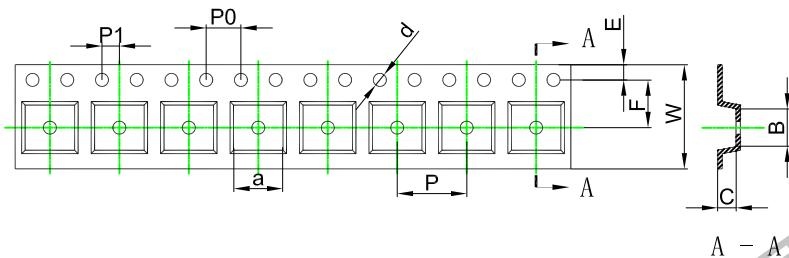


Symbol	Common		
	mm		
	Mim	Nom	Max
A	0.70	0.75	0.85
A1	/	/	0.05
b	0.20	0.30	0.40
c	0.10	0.152	0.25
D	3.15	3.30	3.45
D1	3.00	3.15	3.25
D2	2.29	2.45	2.65
E	3.15	3.30	3.45
E1	2.90	3.05	3.20
E2	1.54	1.74	1.94
E3	0.28	0.48	0.65
E4	0.37	0.57	0.77
E5	0.10	0.20	0.30
e	0.60	0.65	0.70
K	0.59	0.69	0.89
L	0.30	0.40	0.50
L1	0.06	0.125	0.20
t	0	0.075	0.13
Φ	10	12	14

TM80N04DF

N-Channel Enhancement Mosfet

PDFN3x3-8L Embossed Carrier Tape



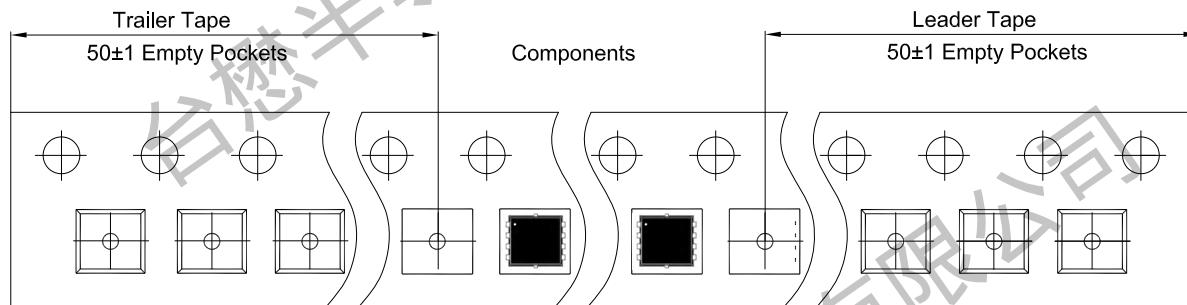
Packaging Description:

SOP-8L parts are shipped in tape. The carrier tape is made from a dissipative (carbon filled) polycarbonate resin. The cover tape is a multilayer film (Heat Activated Adhesive in nature) primarily composed of polyester film, adhesive layer, sealant, and anti-static sprayed agent. These reeled parts in standard option are shipped with 2,500 units per 13" or 33cm diameter reel. The reels are clear in color and is made of polystyrene plastic (anti-static coated).

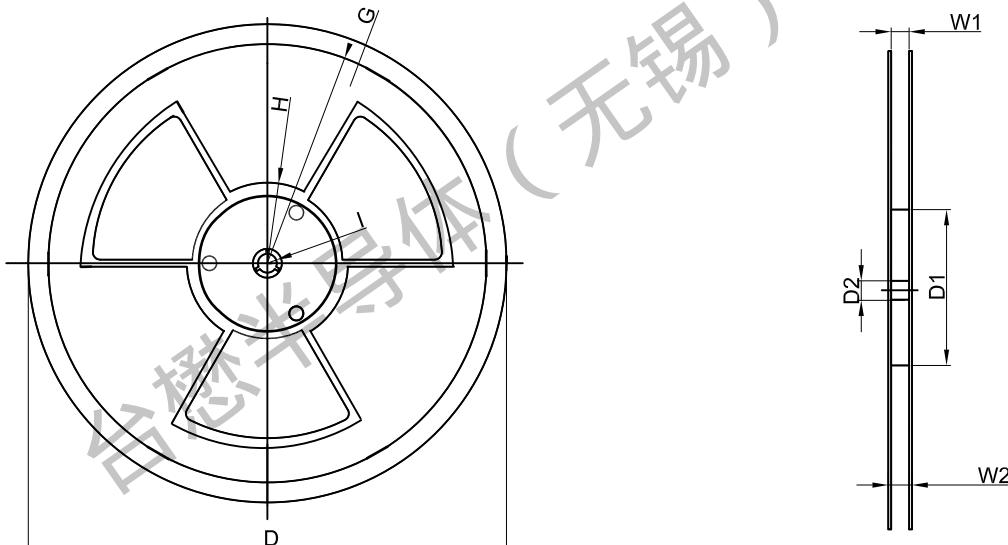
ALL DIM IN mm

Dimensions are in millimeter										
Pkg type	a	B	C	d	E	F	P0	P	P1	W
PDFN3x3-8L	6.40	5.40	2.10	Ø1.50	1.75	5.50	4.00	8.00	2.00	12.00

PDFN3x3-8L Tape Leader and Trailer



PDFN3x3-8L Reel



Dimensions are in millimeter							
Reel Option	D	D1	D2	G	H	I	W1
13" Dia	Ø330.00	100.00	13.00	R135.00	R55.00	R6.50	12.00

REEL	Reel Size	Box	Box Size(mm)	Carton	Carton Size(mm)	G.W.(kg)
5,000 pcs	13 inch	10,000 pcs	370×355×52	50,000 pcs	400×360×368	

Important Notices and Disclaimers

- Tritech-MOS Technology Corp. reserves the right to change this document, its products, and specifications at any time without prior notice.
- Before final design, purchase, or use, customers should obtain and confirm the latest product information and specifications.
- Tritech-MOS Technology Corp. makes no warranties, representations or warranties regarding the suitability of its products for any specific purpose, and Tritech-MOS Technology Corp. does not assume any responsibility for application assistance or customer product design.
- Tritech-MOS Technology Corp. does not guarantee or assume any responsibility for the purchase or use of any unexpected or unauthorized products.
- Any intellectual property rights of Tritech-MOS Technology Corp. are not licensed through implication or other means.
- Products of Tritech-MOS Technology Corp. are not included as critical components in life support equipment or systems without explicit written approval from Tritech-MOS Technology Corp.

Revision history:

Date	Rev	Description	Page
2023.05.10	23.05	Original	