

TM50G03NF

N+P-Channel Enhancement Mode Mosfet

<p>General Description</p> <ul style="list-style-type: none"> • Low $R_{DS(ON)}$ • RoHS and Halogen-Free Compliant <p>Applications</p> <ul style="list-style-type: none"> • Load switch • PWM 	<p>General Features</p> <p>N Channel $V_{DS} = 30V, I_D = 50A$ $R_{DS(ON)} = 7m\Omega (typ.) @ V_{GS} = 10V$</p> <p>P Channel $V_{DS} = -30V, I_D = -45A$ $R_{DS(ON)} = 10m\Omega (typ.) @ V_{GS} = -10V$</p> <p>100% UIS Tested 100% R_g Tested</p>
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NF:DFN5x6-8L

Marking: 50G30 OR 3050

Absolute Maximum Ratings ($T_A = 25^\circ C$ Unless Otherwise Noted)

Symbol	Parameter	Rating		Units
		N-Channel	P-Channel	
V_{DS}	Drain-Source Voltage	30	-30	V
V_{GS}	Gate-Source Voltage	± 20	± 20	V
$I_D @ T_A = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	50	-45	A
$I_D @ T_A = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	32	-29	A
I_{DM}	Pulsed Drain Current ²	185	-166	A
EAS	Single Pulse Avalanche Energy ³	38	76	mJ
$P_D @ T_C = 25^\circ C$	Total Power Dissipation ⁴	15	21.3	W
T_{STG}	Storage Temperature Range	-55 to 175	-55 to 175	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 175	-55 to 175	$^\circ C$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	---	48	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	5	$^\circ C/W$

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N-Channel Electrical Characteristics $T = 25^{\circ}\text{C}$ unless otherwise specified

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	30	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $I_D=1\text{mA}$	---	0.027	---	$V/^{\circ}\text{C}$
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=10V, I_D=12A$	---	7.0	12	m Ω
		$V_{GS}=4.5V, I_D=10A$	---	12	14	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	1.2	1.6	2.0	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	-5.8	---	mV/ $^{\circ}\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=24V, V_{GS}=0V, T_J=25^{\circ}\text{C}$	---	---	1	uA
		$V_{DS}=24V, V_{GS}=0V, T_J=55^{\circ}\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA
R_g	Gate Resistance	$V_{DS}=0V, V_{GS}=0V, f=1\text{MHz}$	---	1.7	---	Ω
Q_g	Total Gate Charge (4.5V)		---	12.8	---	nC
Q_{gs}	Gate-Source Charge	$V_{DS}=20V, V_{GS}=4.5V, I_D=12A$	---	3.3	---	
Q_{gd}	Gate-Drain Charge		---	6.5	---	
$T_{d(on)}$	Turn-On Delay Time		---	4.5	---	ns
T_r	Rise Time	$V_{DD}=12V, V_{GS}=10V, R_G=3.3\Omega$	---	10.8	---	
$T_{d(off)}$	Turn-Off Delay Time	$I_D=5A$	---	25.5	---	
T_f	Fall Time		---	9.6	---	
C_{iss}	Input Capacitance		---	1200	---	pF
C_{oss}	Output Capacitance	$V_{DS}=15V, V_{GS}=0V, f=1\text{MHz}$	---	163	---	
C_{rss}	Reverse Transfer Capacitance		---	131	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current ^{1,6}	$V_G=V_D=0V$, Force Current	---	---	50	A
I_{SM}	Pulsed Source Current ^{2,6}		---	---	32	A
V_{SD}	Diode Forward Voltage ²	$V_{GS}=0V, I_S=1A, T_J=25^{\circ}\text{C}$	---	---	1.2	V

Note :

- The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- The EAS data shows Max. rating. The test condition is $V_{DD}=25V, V_{GS}=10V, L=0.1\text{mH}, I_{AS}=34A$
- The power dissipation is limited by 150°C junction temperature
- The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.

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P-Channel Electrical Characteristics ($T_J=25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=-30V, V_{GS}=0V,$	-	-	-1	μA
I_{GSS}	Gate to Body Leakage Current	$V_{DS}=0V, V_{GS}=\pm 20V$	-	-	± 100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1.0	-1.5	-2.0	V
$R_{DS(on)}$	Static Drain-Source on-Resistance Note3	$V_{GS}=-10V, I_D=-10A$	-	10	16	m Ω
		$V_{GS}=-4.5V, I_D=-5A$	-	15	24	
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS}=-15V, V_{GS}=0V,$ $f=1.0\text{MHz}$	-	1770	-	pF
C_{oss}	Output Capacitance		-	233	-	pF
C_{rss}	Reverse Transfer Capacitance		-	206	-	pF
Q_g	Total Gate Charge	$V_{DS}=-15V, I_D=-5A,$ $V_{GS}=-10V$	-	22	-	nC
Q_{gs}	Gate-Source Charge		-	1.0	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	1.8	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-on Delay Time	$V_{DD}=-15V, I_D=-10A,$ $V_{GS}=-10V, R_{GEN}=2.5\Omega$	-	9	-	ns
t_r	Turn-on Rise Time		-	13	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	48	-	ns
t_f	Turn-off Fall Time		-	20	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	-45	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-29	A
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS}=0V, I_S=-15A$	-	-0.8	-1.2	V
t_{rr}	Reverse Recovery Time	$T_J=25^{\circ}\text{C},$	-	64	-	ns
Q_{rr}	Reverse Recovery Charge	$V_{DD}=-24V, I_F=-2.8A,$ $dI/dt=-100A/\mu s$	-	25	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition: $T_J=25^{\circ}\text{C}, V_{GS}=10V, R_G=25\Omega, L=0.5\text{mH}, I_{AS}=-12.7A$

3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 0.5\%$



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Typical Performance Characteristics-N

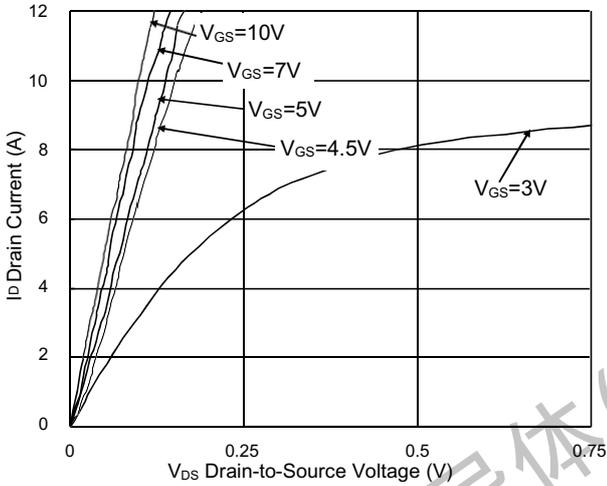


Fig.1 Typical Output Characteristics

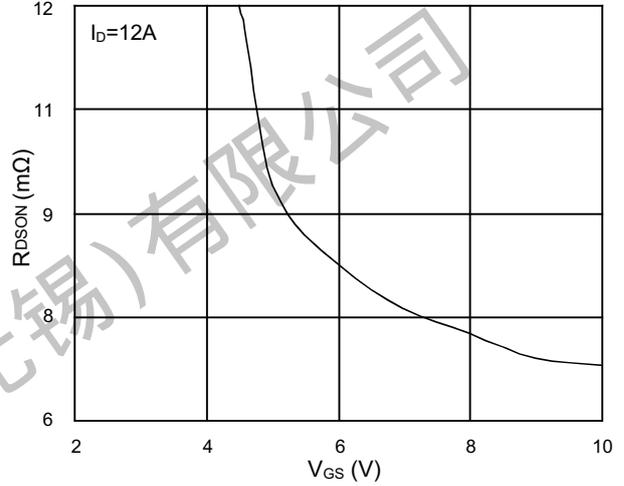


Fig.2 On-Resistance vs. G-S Voltage

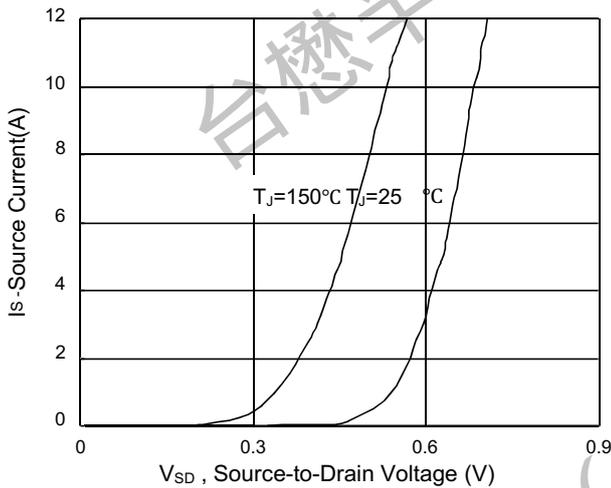


Fig.3 Forward Characteristics of Reverse

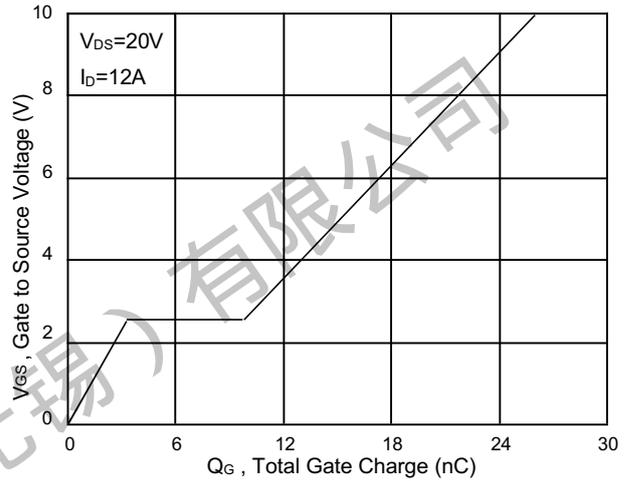


Fig.4 Gate-Charge Characteristics

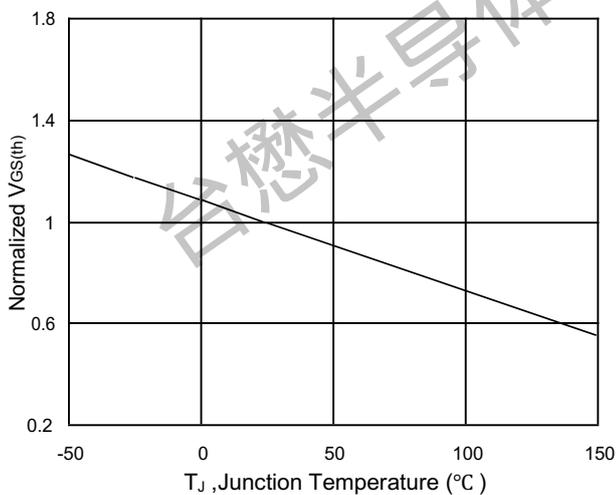


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

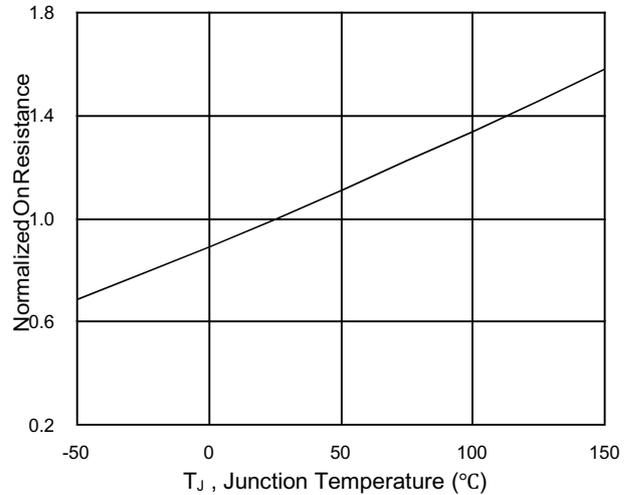


Fig.6 Normalized $R_{DS(on)}$ vs. T_J



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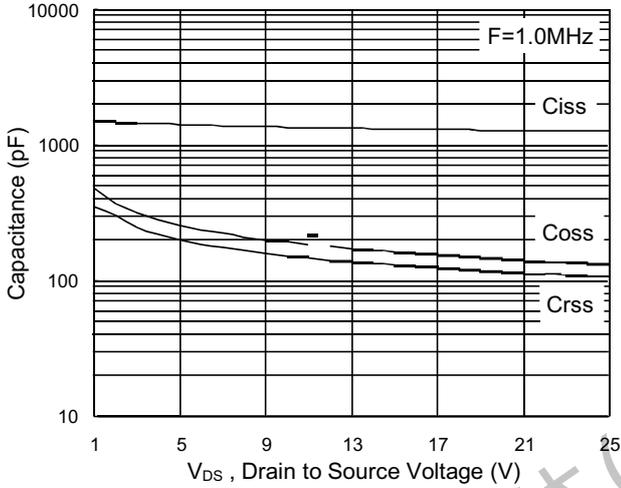


Fig.7 Capacitance

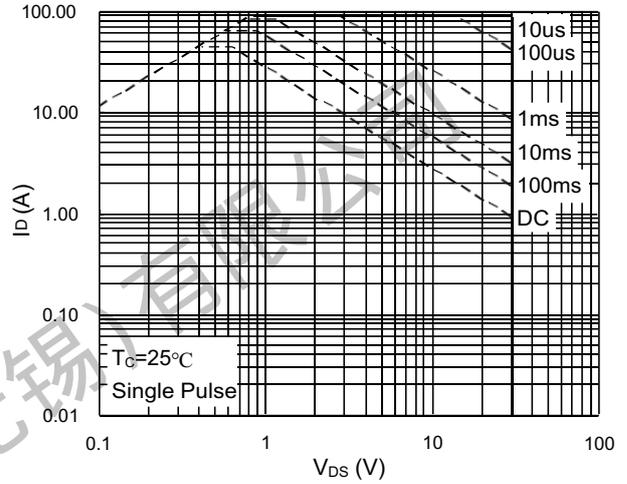


Fig.8 Safe Operating Area

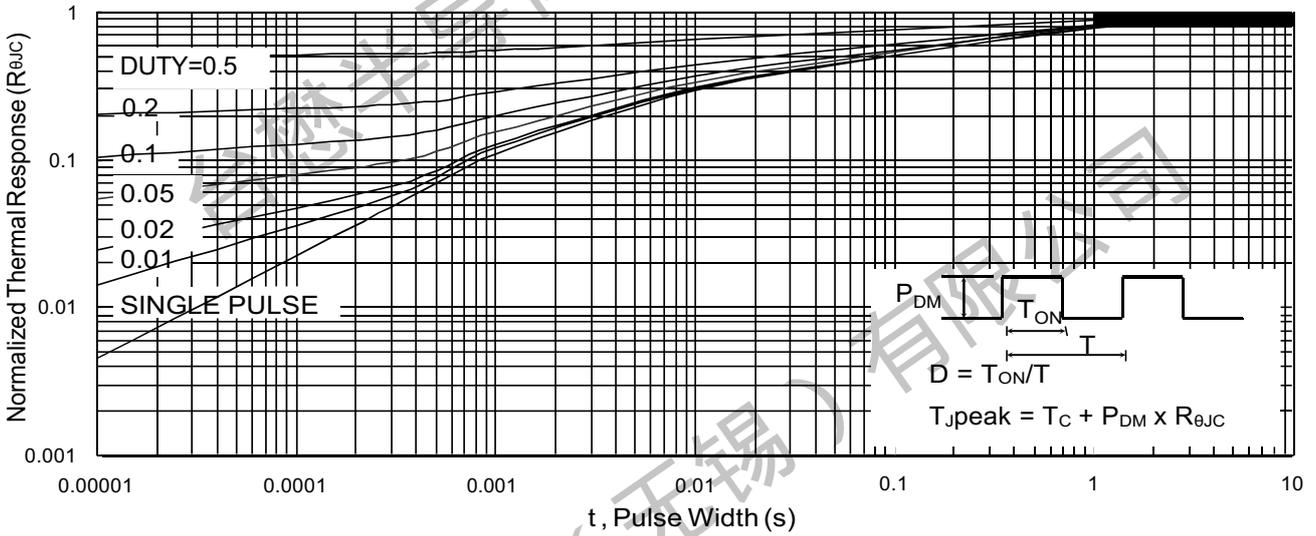


Fig.9 Normalized Maximum Transient Thermal Impedance

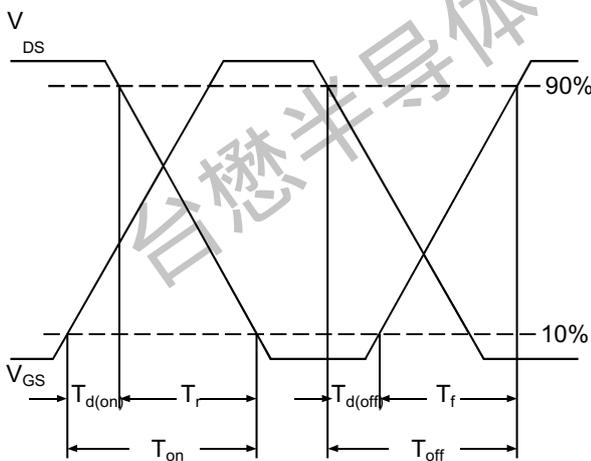


Fig.10 Switching Time Waveform

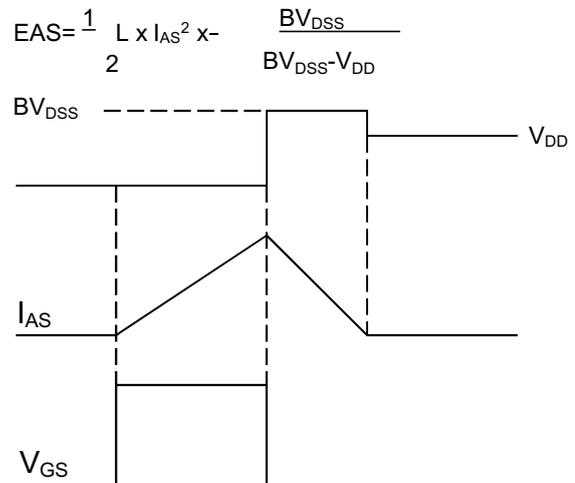


Fig.11 Unclamped Inductive Switching Waveform

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Typical Performance Characteristics-P

Figure 1: Output Characteristics

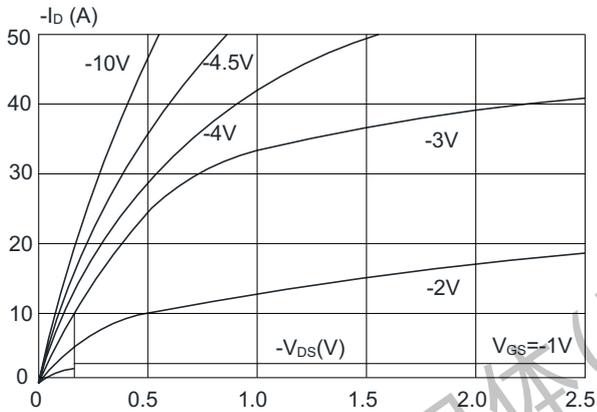


Figure 2: Typical Transfer Characteristics

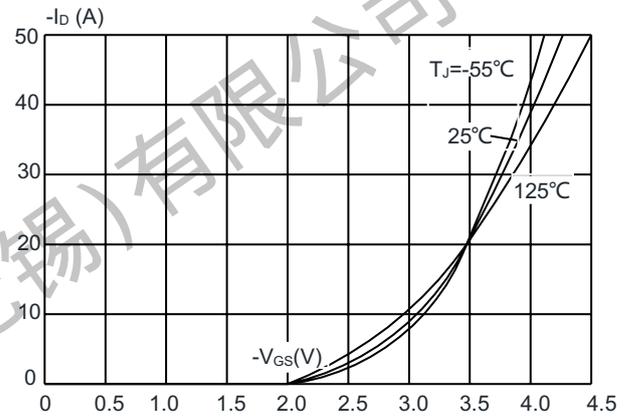


Figure 3: On-resistance vs. Drain Current

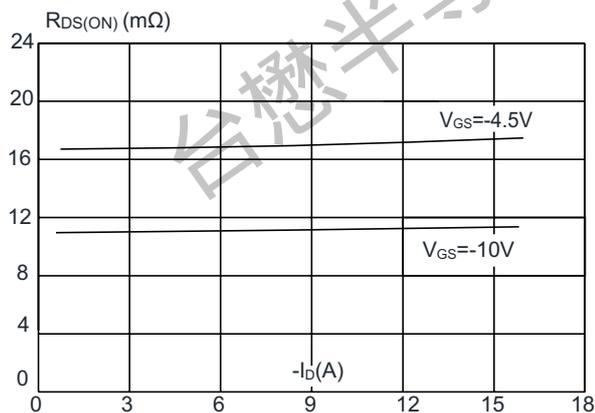


Figure 4: Body Diode Characteristics

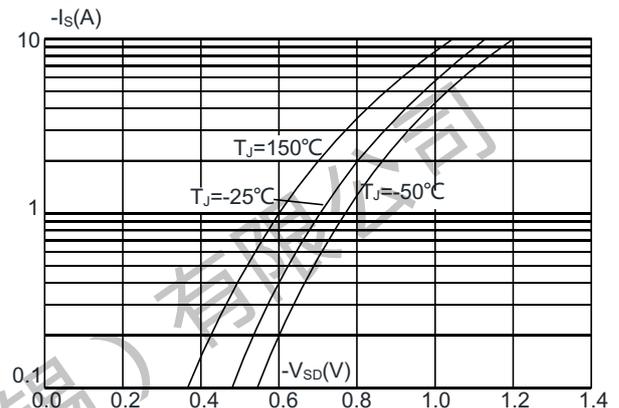


Figure 5: Gate Charge Characteristics

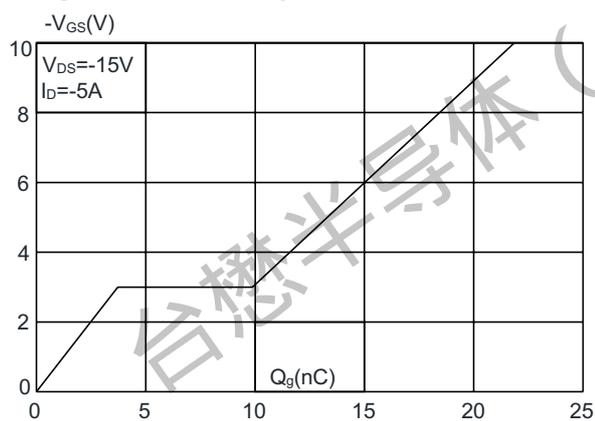
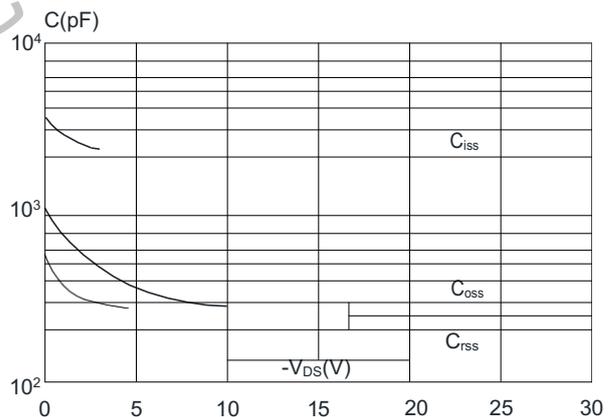


Figure 6: Capacitance Characteristics



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Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

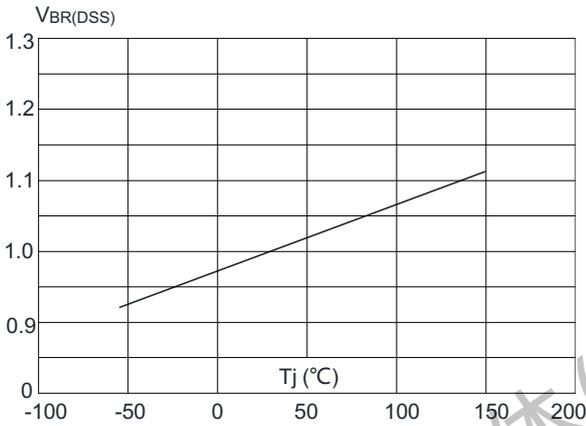


Figure 8: Normalized on Resistance vs. Junction Temperature

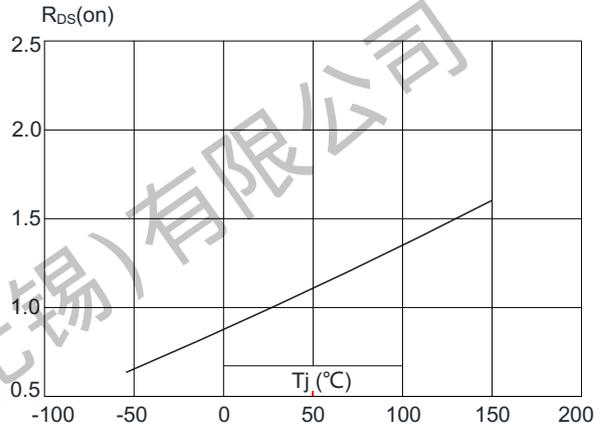


Figure 9: Maximum Safe Operating Area

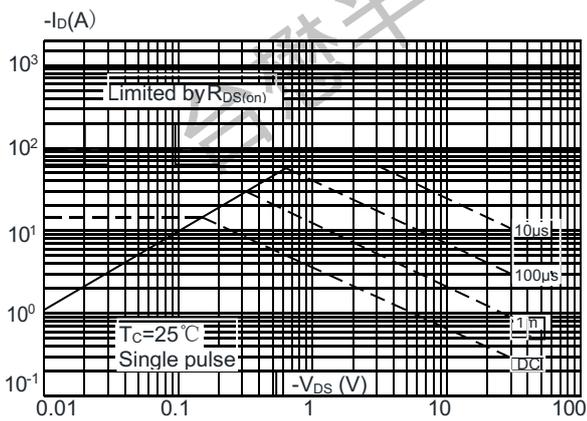


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

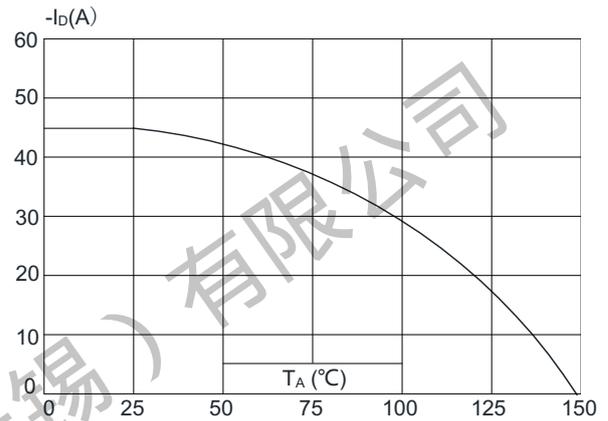
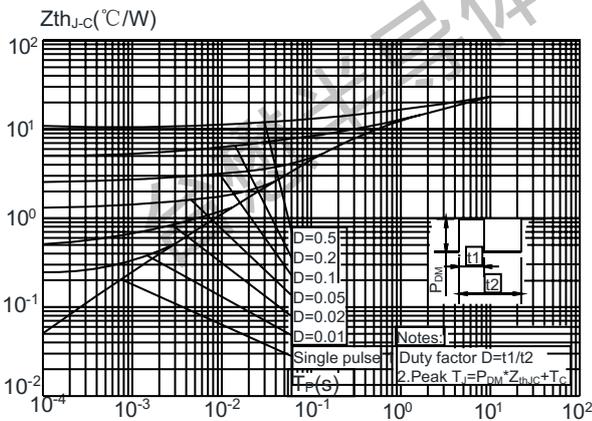


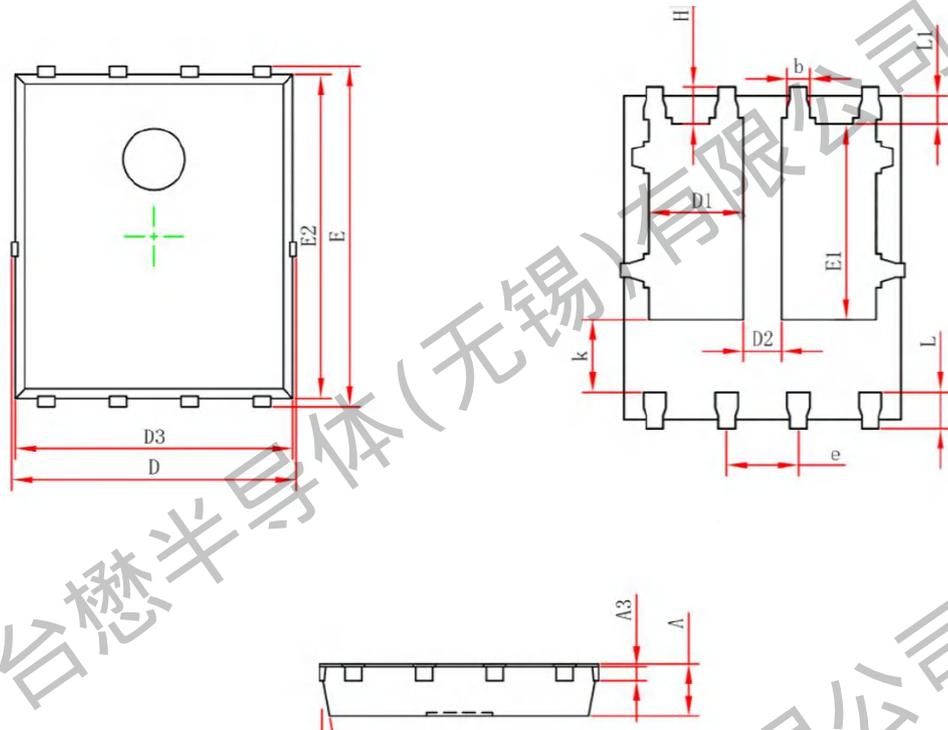
Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case



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Package Mechanical Data:DFN5x6-8L

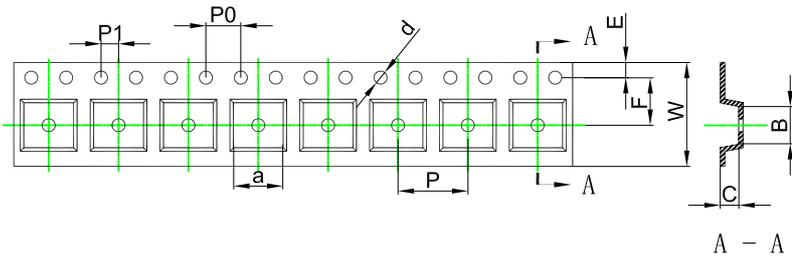


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.000	0.035	0.039
A3	0.154REF.		0.006REF.	
D	4.944	5.096	0.195	0.201
E	5.974	6.126	0.235	0.241
D1	1.470	1.870	0.058	0.074
D2	0.470	0.870	0.019	0.034
E1	3.375	3.575	0.133	0.141
D3	4.824	4.976	0.190	0.196
E2	5.674	5.826	0.223	0.229
k	1.190	1.390	0.047	0.055
b	0.350	0.450	0.014	0.018
e	1.270TYP.		0.050TYP.	
L	0.559	0.711	0.022	0.028
L1	0.424	0.576	0.017	0.023
H	0.574	0.726	0.023	0.029
θ	10°	12°	10°	12°

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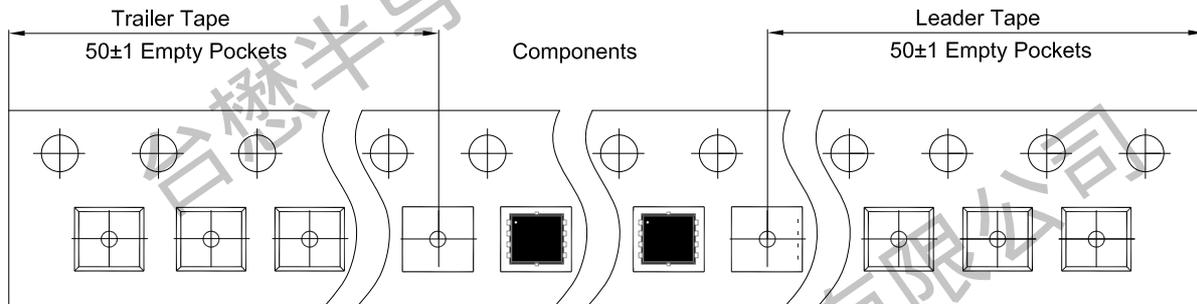
PDFN5x6-8L Embossed Carrier Tape



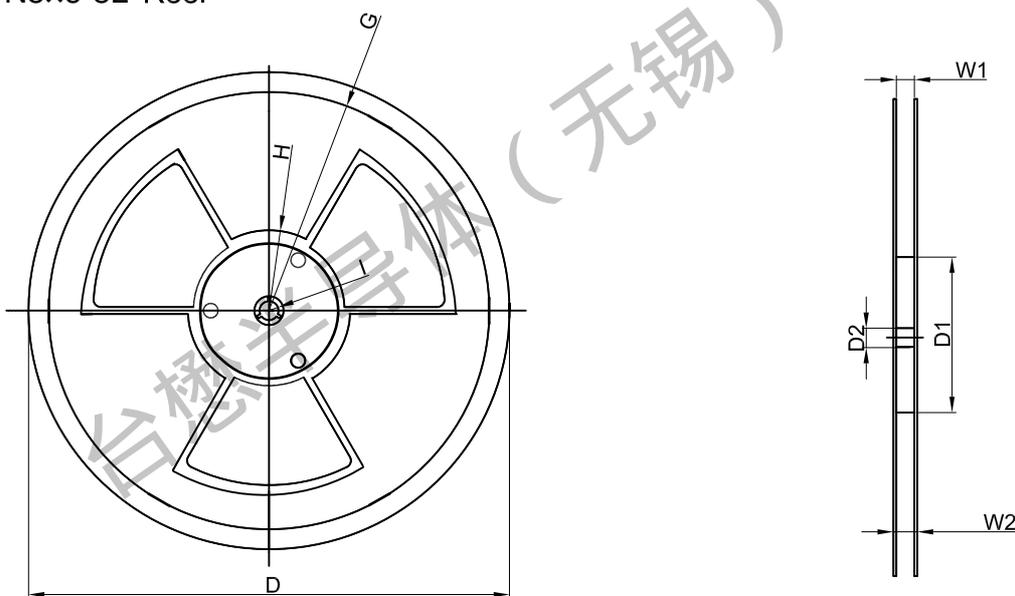
Packaging Description:
SOP-8L parts are shipped in tape. The carrier tape is made from a dissipative (carbon filled) polycarbonate resin. The cover tape is a multilayer film (Heat Activated Adhesive in nature) primarily composed of polyester film, adhesive layer, sealant, and anti-static sprayed agent. These reeled parts in standard option are shipped with 2,500 units per 13" or 33cm diameter reel. The reels are clear in color and is made of polystyrene plastic (anti-static coated).
ALL DIM IN mm

Dimensions are in millimeter										
Pkg type	a	B	C	d	E	F	P0	P	P1	W
PDFN5x6-8L	6.40	5.40	2.10	Ø1.50	1.75	5.50	4.00	8.00	2.00	12.00

PDFN5x6-8L Tape Leader and Trailer



PDFN5x6-8L Reel



Dimensions are in millimeter								
Reel Option	D	D1	D2	G	H	I	W1	W2
13"Dia	Ø330.00	100.00	13.00	R135.00	R55.00	R6.50	12.00	14.00

REEL	Reel Size	Box	Box Size(mm)	Carton	Carton Size(mm)	G.W.(kg)
5,000 pcs	13 inch	10,000 pcs	370×355×52	50,000 pcs	400×360×368	

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Revision history:

Date	Rev	Description	Page
2023.09.16	23.09	Original	