TM20G06GD

N+P-Channel Enhancement Mode Mosfet

General Description

- Low $R_{DS(ON)}$
- RoHS and Halogen-Free Compliant

Applications

- Load switch
- PWM

General Features

N Channel

V_{DS} = 60V I_D =20A

$R_{DS(ON)}$ =30mΩ (typ.) @V_{GS}= 10V

P Channel

V_{DS} = -60V I_D =-15A

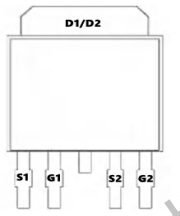
$R_{DS(ON)}$ =70mΩ (typ.) @V_{GS}=-10V

100% UIS Tested

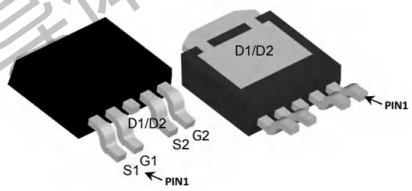
100% R_g Tested

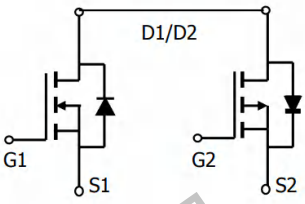
Green Product

GD:TO-252-4L



Marking: 6020





Absolute Maximum Ratings (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Rating		Units
		N-Ch	P-Ch	
V_{DS}	Drain-Source Voltage	60	-60	V
V_{GS}	Gate-Source Voltage	±20	±20	V
I_D@Ta=25°C	Continuous Drain Current, V_{GS} @ 10V[1]	20	-15	A
I_D@Ta=100°C	Continuous Drain Current, V_{GS} @ 10V[1]	13	-9.8	A
I_{DM}	Pulsed Drain Current[2]	74	-56	A
EAS	Single Pulse Avalanche Energy[3]	22	29.8	mJ
P_D@T_C=25°C	Total Power Dissipation[4]	50	50	W
I_{AS}	Avalanche Current, Single pulse	21	-24.4	A
T_{STG}	Storage Temperature Range	-55 to 175	-55 to 175	°C
T_J	Operating Junction Temperature Range	-55 to 175	-55 to 175	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient [1]	---	60	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case[1]	---	3.8	°C/W

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N-Channel Electrical Characteristics (T_J=25 ℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=250uA	60	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance[2]	V_{GS}=10V , I_D=15A	---	30	40	mΩ
		V_{GS}=4.5V , I_D=7A	---	35	45	
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =250uA	1.0	2.0	3.0	V
I_{DSS}	Drain-Source Leakage Current	V_{DS}=48V , V_{GS}=0V , T_J=25°C	---	---	1	uA
		V_{DS}=48V , V_{GS}=0V , T_J=55°C	---	---	5	
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA
gfs	Forward Transconductance	V_{DS}=5V , I_D=15A	---	25.3	---	S
Q_g	Total Gate Charge (10V)	V_{DS}=48V , V_{GS}=10V , I_D=15A	---	19	---	nC
Q_{gs}	Gate-Source Charge		---	2.5	---	
Q_{gd}	Gate-Drain Charge		---	5	---	
$T_{d(on)}$	Turn-On Delay Time	V_{DD}=30V , V_{GS}=10V , R_G=3.3Ω I_D=15A	---	2.8	---	ns
T_r	Rise Time		---	16.6	---	
$T_{d(off)}$	Turn-Off Delay Time		---	21.2	---	
T_f	Fall Time		---	5.6	---	
C_{iss}	Input Capacitance	V_{DS}=15V , V_{GS}=0V , f=1MHz	---	1027	---	pF
C_{oss}	Output Capacitance		---	65	---	
C_{rss}	Reverse Transfer Capacitance		---	46	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current[1,6]	V_G=V_D=0V , Force Current	---	---	20	A
V_{SD}	Diode Forward Voltage[2]	V_{GS}=0V , I_S=1A , T_J=25°C	---	---	1.2	V

Note :
1.The data tested by surface mounted on a 1 inch^2 FR-4 board with 2OZ copper.
2.The data tested by pulsed , pulse width ≦ 300us , duty cycle ≦ 2%
3.The EAS data shows Max. rating . The test condition is V_{DD}=25V,V_{GS}=10V,L=0.1mH,I_{AS}=21A
4.The power dissipation is limited by 150℃ junction temperature
5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

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P-Channel Electrical Characteristics (T_J=25 ℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=-250uA	-60	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance[2]	V_{GS}=-10V , I_D=-10A	---	70	80	mΩ
		V_{GS}=-4.5V , I_D=-5A	---	85	100	
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =-250uA	-1.0	-2.0	-3.0	V
I_{DSS}	Drain-Source Leakage Current	V_{DS}=-48V , V_{GS}=0V , T_J=25℃	---	---	1	uA
		V_{DS}=-48V , V_{GS}=0V , T_J=55℃	---	---	5	
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA
gfs	Forward Transconductance	V_{DS}=-5V , I_D=-4A	---	8.7	---	S
Q_g	Total Gate Charge (-4.5V)	V_{DS}=-12V , V_{GS}=-4.5V , I_D=-6A	---	11.8	---	nC
Q_{gs}	Gate-Source Charge		---	1.9	---	
Q_{gd}	Gate-Drain Charge		---	6.5	---	
$T_{d(on)}$	Turn-On Delay Time	V_{DD}=-15V , V_{GS}=-10V , R_G=3.3Ω, I_D=-1A	---	8.8	---	ns
T_r	Rise Time		---	19.6	---	
$T_{d(off)}$	Turn-Off Delay Time		---	47.2	---	
T_f	Fall Time		---	9.6	---	
C_{iss}	Input Capacitance	V_{DS}=-15V , V_{GS}=0V , f=1MHz	---	1080	---	pF
C_{oss}	Output Capacitance		---	73	---	
C_{rss}	Reverse Transfer Capacitance		---	50	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current[1,5]	V_G=V_D=0V , Force Current	---	---	-15	A
V_{SD}	Diode Forward Voltage[2]	V_{GS}=0V , I_S=-1A , T_J=25℃	---	---	-1	V

Note :
1.The data tested by surface mounted on a 1 inch^2 FR-4 board with 2OZ copper.
2.The data tested by pulsed , pulse width ≦ 300us , duty cycle ≦ 2%
3.The EAS data shows Max. rating . The test condition is V_{DD}=-25V,V_{GS}=-10V,L=0.1mH,I_{AS}=-24.4A
4.The power dissipation is limited by 150℃ junction temperature
5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

N-Channel Typical Characteristics

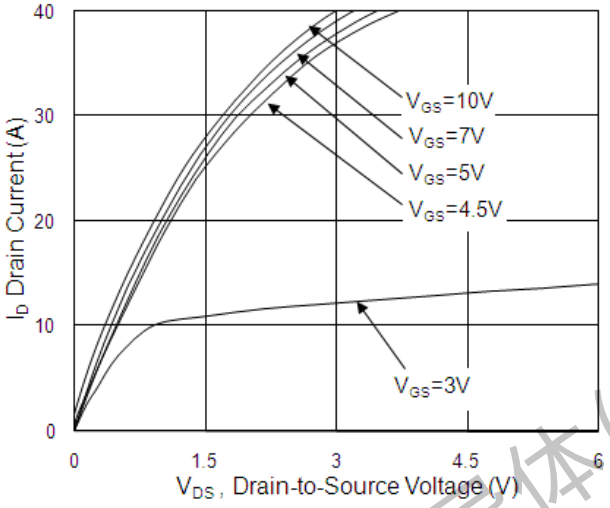

Fig.1 Typical Output Characteristics

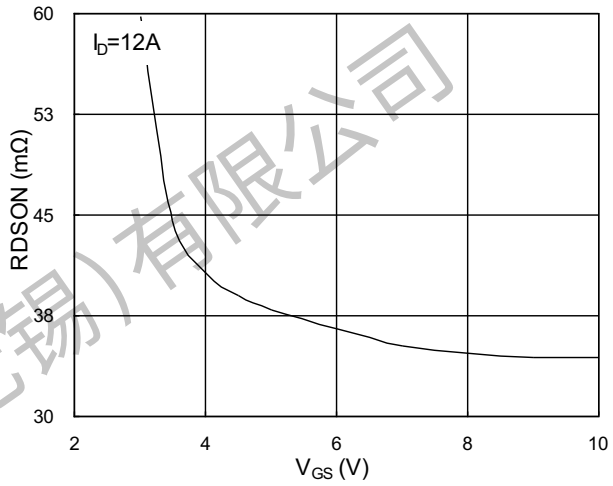

Fig.2 On-Resistance vs. G-S Voltage

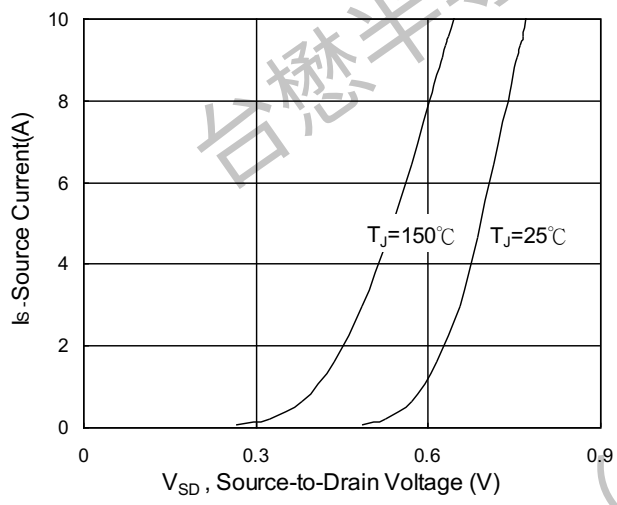

Fig.3 Source Drain Forward Characteristics

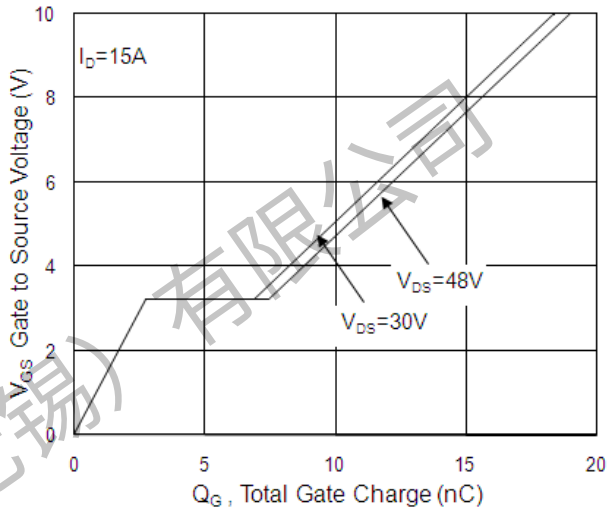

Fig.4 Gate-Charge Characteristics

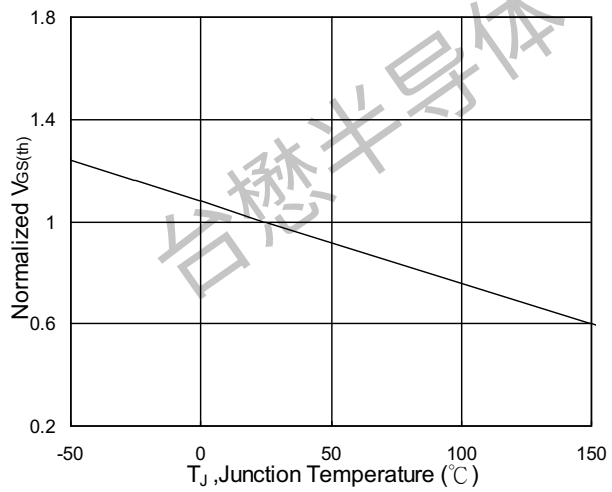

Fig.5 Normalized $V_{GS(th)}$ vs. T_J

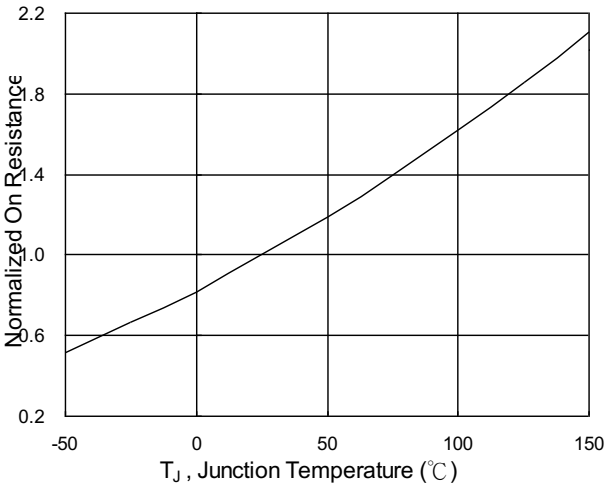

Fig.6 Normalized R_{DSON} vs. T_J

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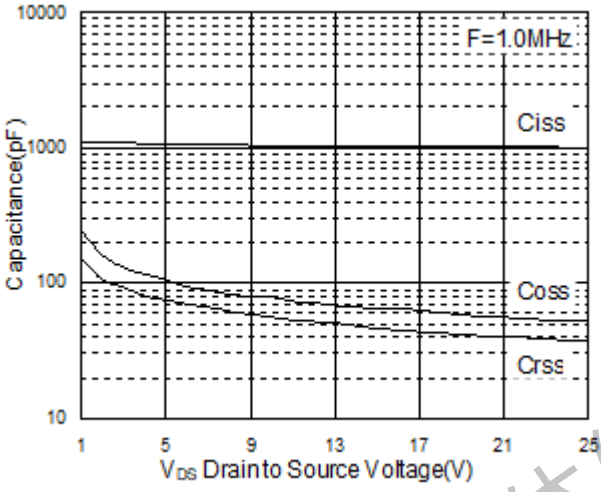

Fig.7 Capacitance

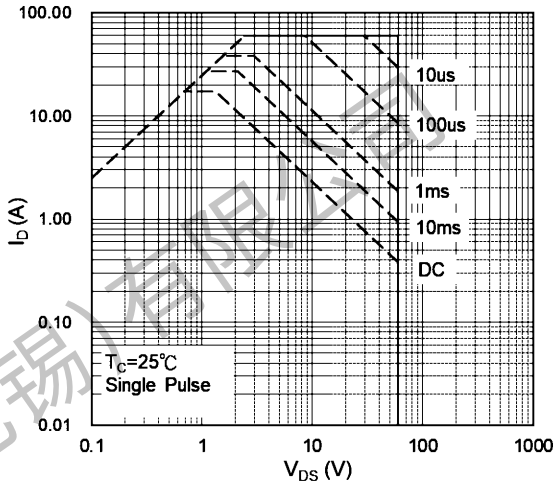

Fig.8 Safe Operating Area

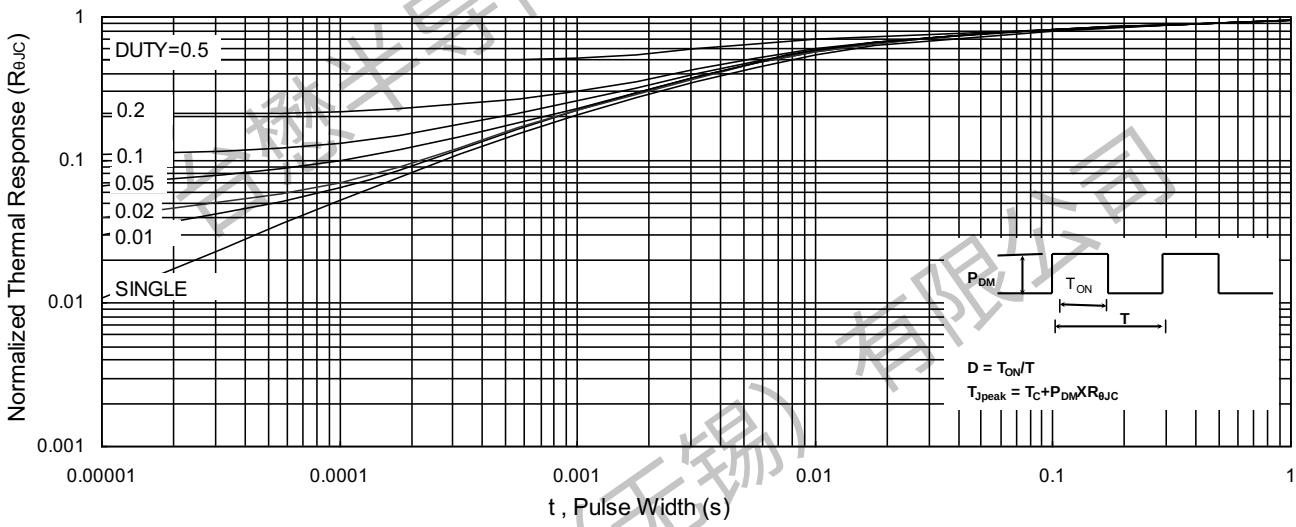

Fig.9 Normalized Maximum Transient Thermal Impedance

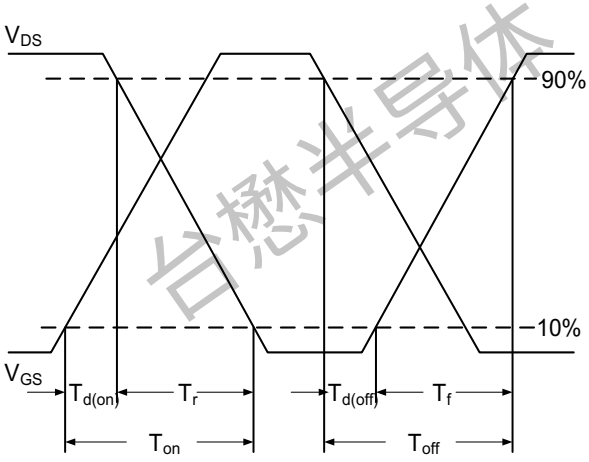

Fig.10 Switching Time Waveform

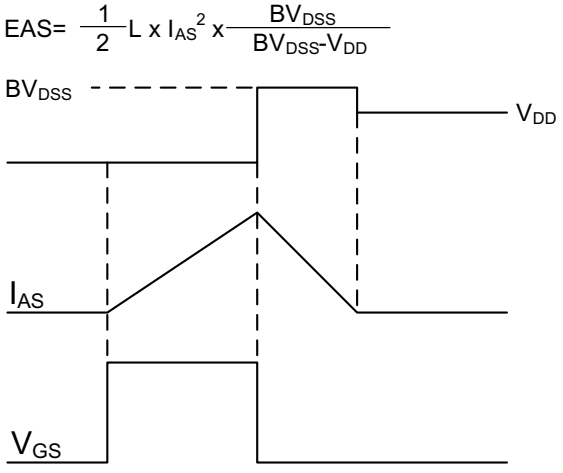

Fig.11 Unclamped Inductive Switching Waveform

P-Channel Typical Characteristics

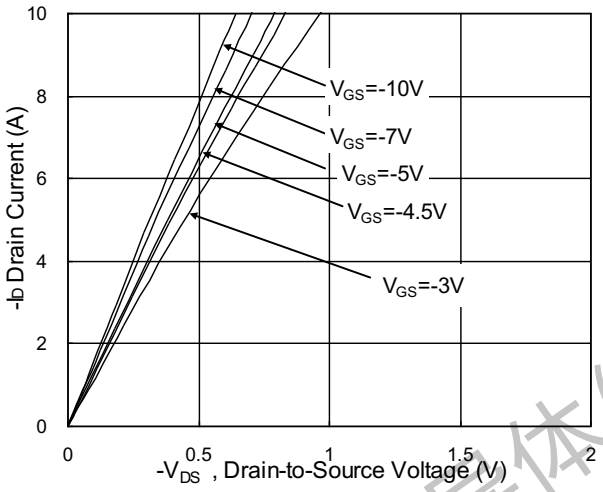

Fig.1 Typical Output Characteristics

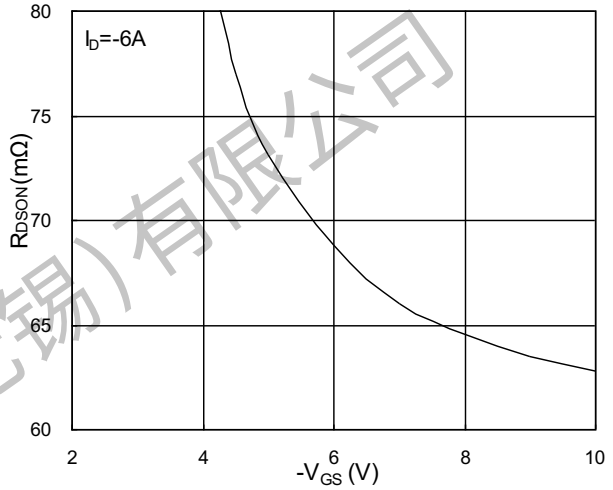

Fig.2 On-Resistance vs. G-S Voltage

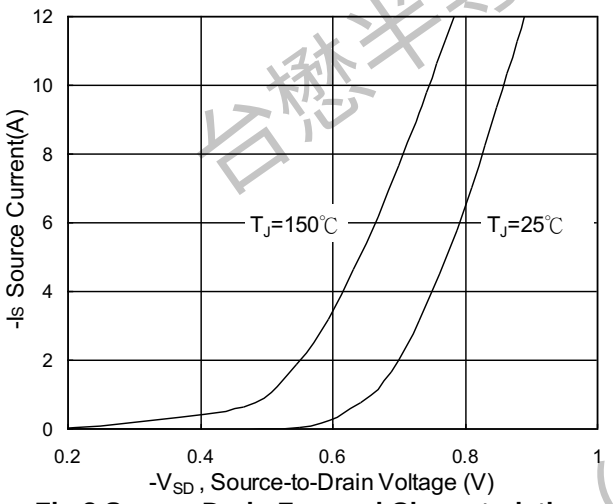

Fig.3 Source Drain Forward Characteristics

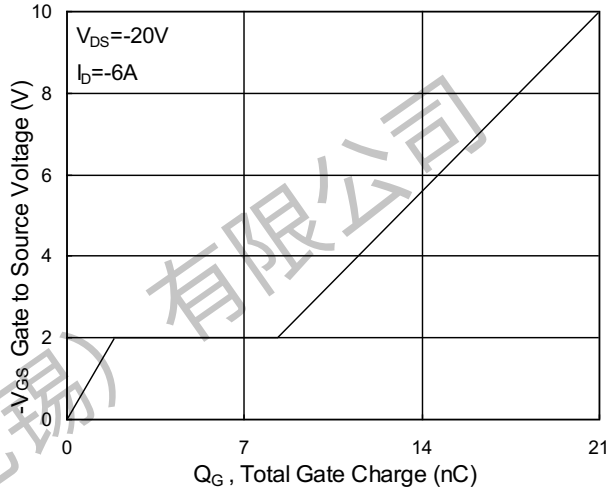

Fig.4 Gate-Charge Characteristics

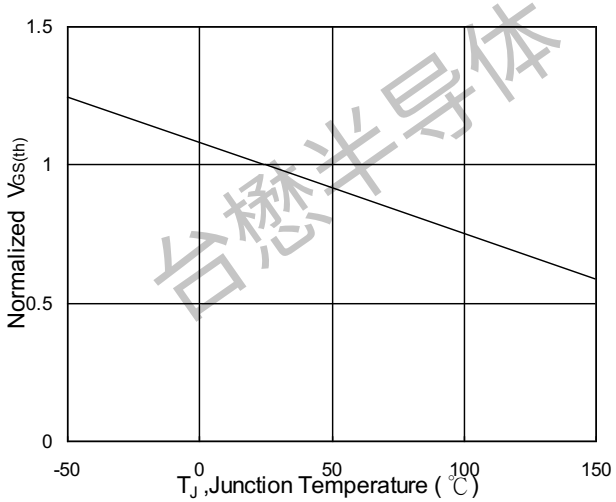

Fig.5 Normalized $V_{GS(th)}$ vs. T_J

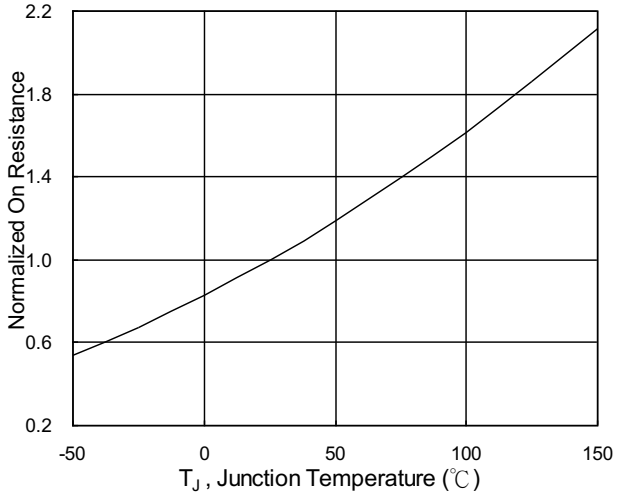

Fig.6 Normalized R_{DSON} vs. T_J

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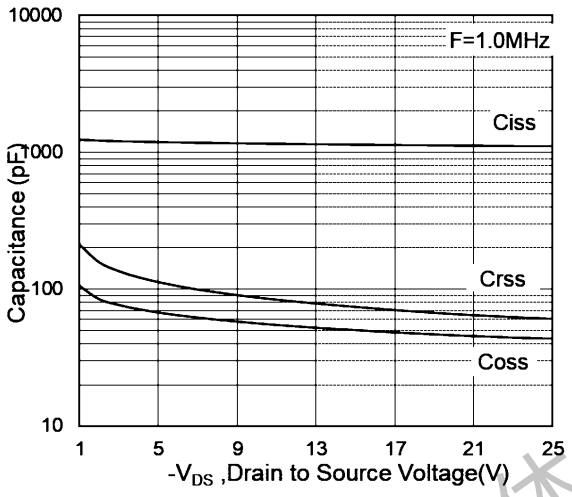

Fig.7 Capacitance

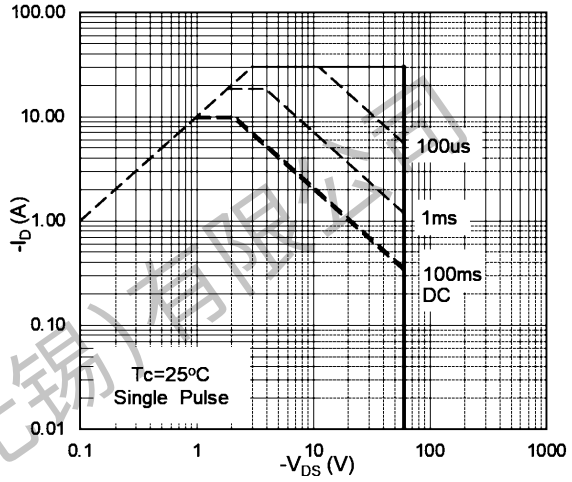

Fig.8 Safe Operating Area

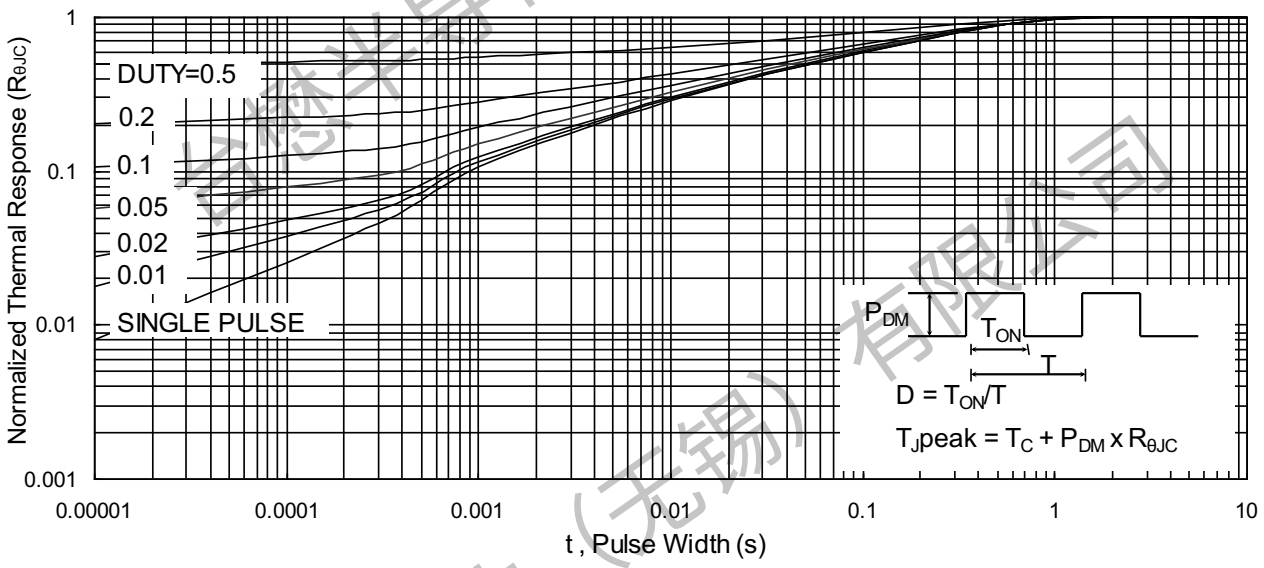

Fig.9 Normalized Maximum Transient Thermal Impedance

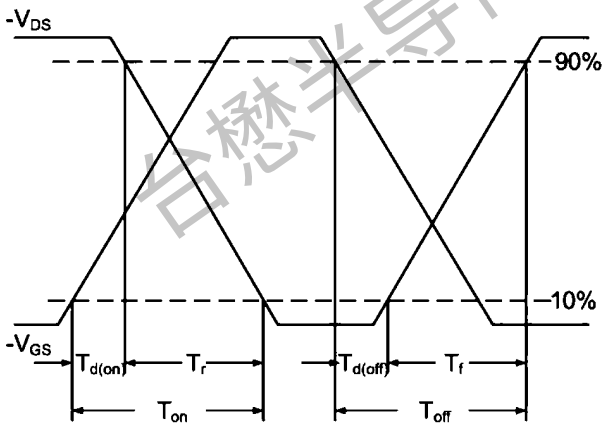

Fig.10 Switching Time Waveform

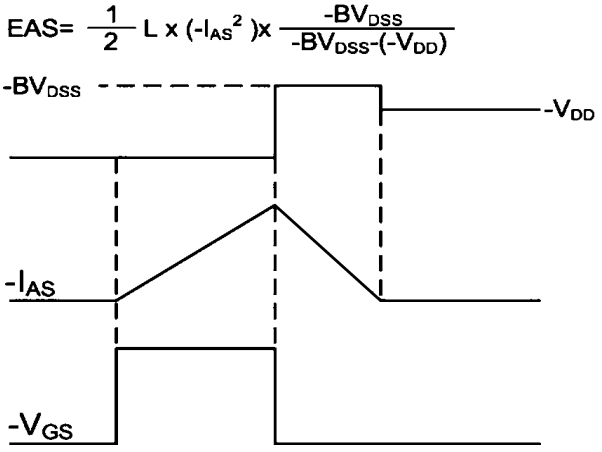

Fig.11 Unclamped Inductive Switching Waveform

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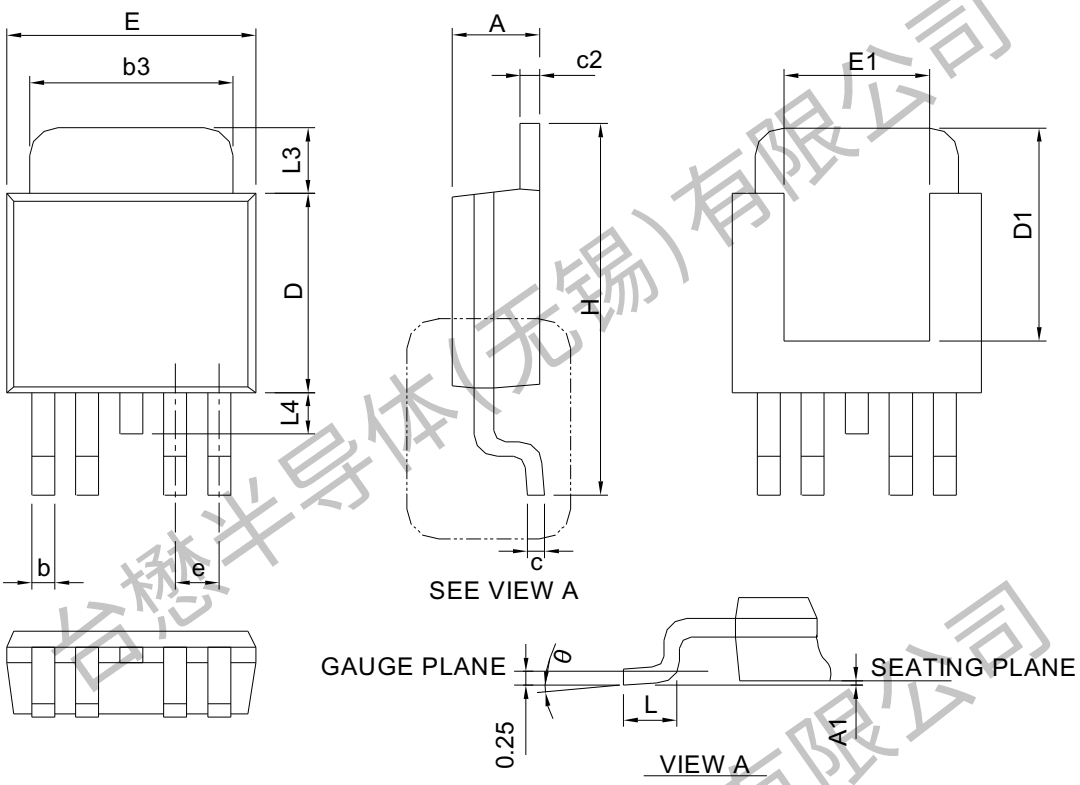
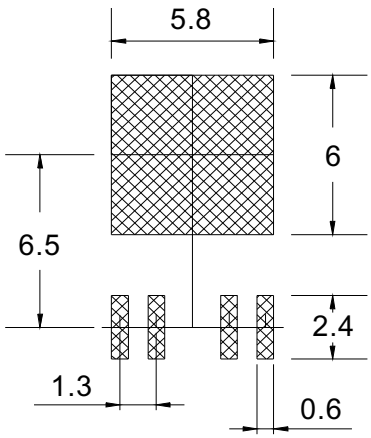
Package Mechanical Data:TO-252-4L



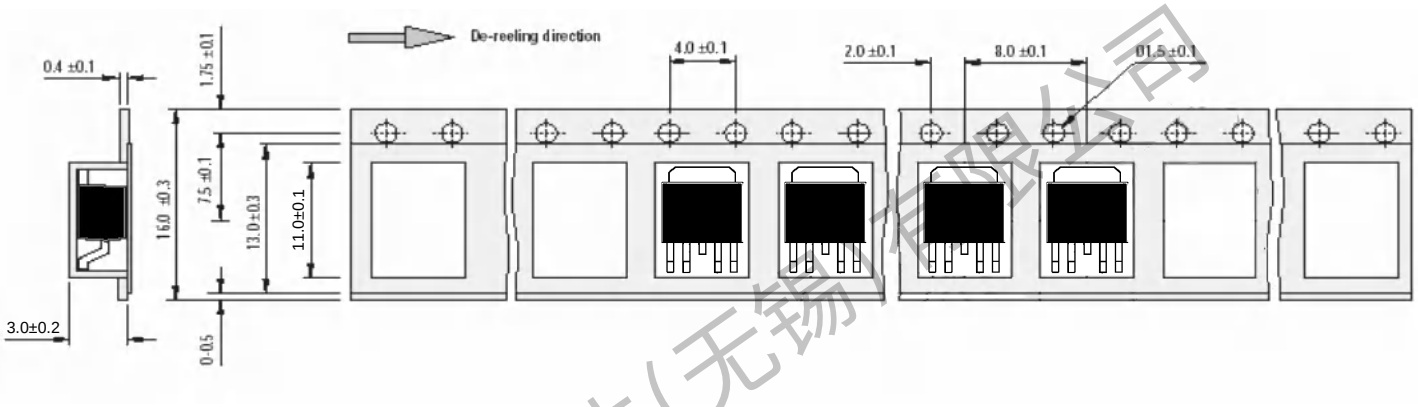
SYMBOL	TO-252-4			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	2.18	2.39	0.086	0.094
A1	-	0.2	-	0.008
b	0.50	0.71	0.020	0.028
b3	4.32	5.46	0.170	0.215
c	0.46	0.61	0.018	0.024
c2	0.46	0.89	0.018	0.035
D	5.33	6.22	0.210	0.245
D1	4.57	6.00	0.180	0.236
E	6.35	6.73	0.250	0.265
E1	3.81	6.00	0.150	0.236
e	1.30 BSC		0.051 BSC	
H	9.40	10.41	0.370	0.410
L	1.40	1.78	0.055	0.070
L3	0.89	2.03	0.035	0.080
L4	-	1.02	-	0.040
θ	0°	8°	0°	8°

RECOMMENDED LAND PATTERN

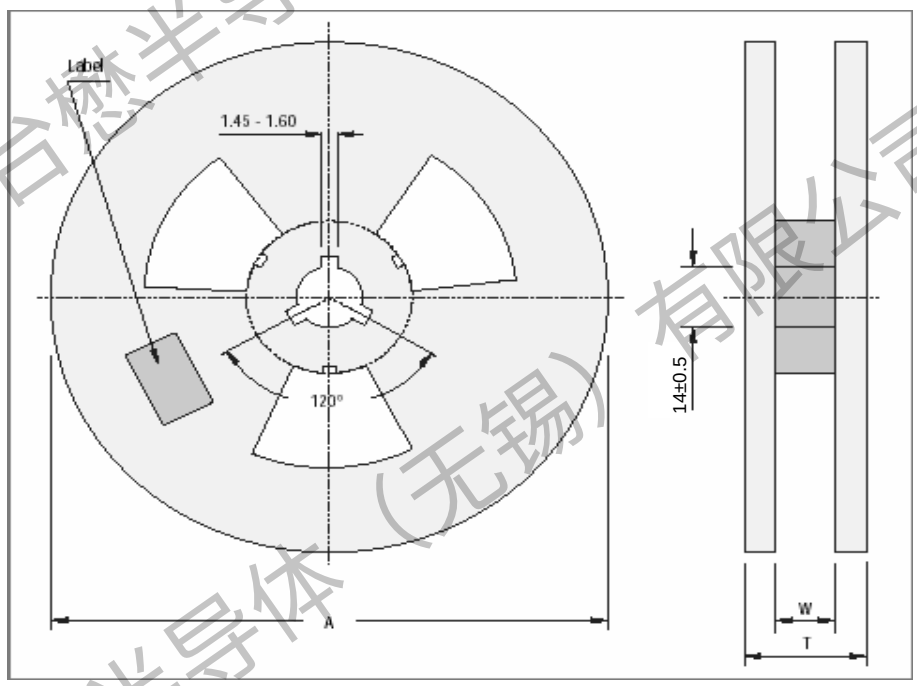


UNIT: mm

TO-252-4L Embossed Carrier Tape



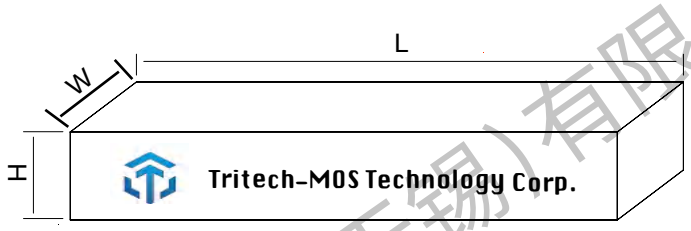
TO-252-4L ReeL



1.TO-252-4L Packaging

Package	Packing Form	Quantity		
		ReeL	Inner Box	Outbox
TO-252-4L	ReeL	2500	5	1

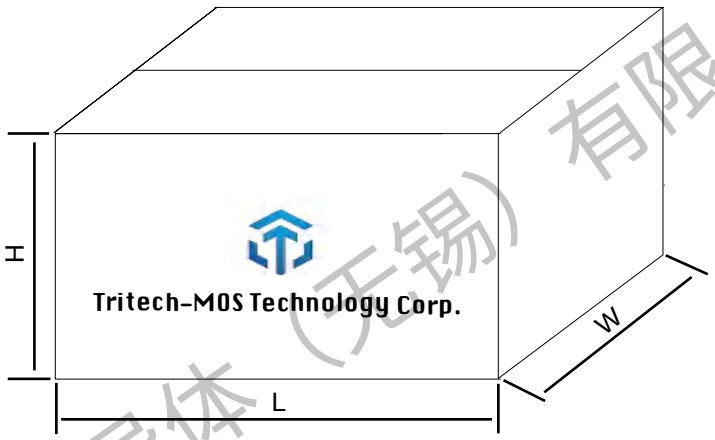
Inner Box



Dimension : 370 (L)×355(W) ×50(H) mm

Quantity : 2500 × 2Ea = 5000pcs

Outer Box



Dimension : 380(L)×380(W) ×275(H) mm

Quantity : 5000 × 5Ea = 25000pcs

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Revision history:

Date	Rev	Description	Page
2023.04.04	23.04	Original	