

# TM20G03GD

## N+P-Channel Enhancement Mode Mosfet

### General Description

- Low  $R_{DS(ON)}$
- RoHS and Halogen-Free Compliant

### Applications

- Load switch
- PWM

### General Features

#### N Channel

$V_{DS} = 30V$   $I_D = 23A$

$R_{DS(ON)} = 15m\Omega$  (typ.) @  $V_{GS} = 10V$

#### P Channel

$V_{DS} = -30V$   $I_D = -17A$

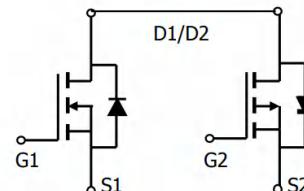
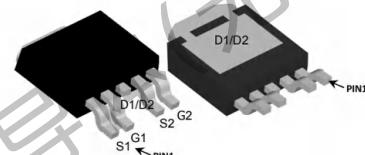
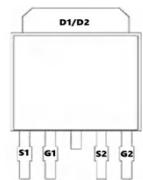
$R_{DS(ON)} = 33m\Omega$  (typ.) @  $V_{GS} = -10V$

100% UIS Tested

100%  $R_g$  Tested



**GD:TO-252-4L**



Marking: 20G03 OR 3012

### Absolute Maximum Ratings ( $T_A = 25^\circ C$ Unless Otherwise Noted)

Symbol	Parameter	Rating		Units
		N-Channel	P-Channel	
$V_{DS}$	Drain-Source Voltage	30	-30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	$\pm 20$	V
$I_D @ T_A = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	23	-17	A
$I_D @ T_A = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	15	-12	A
$I_{DM}$	Pulsed Drain Current	55	-43	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	34.6	28	mJ
$P_D @ T_C = 25^\circ C$	Total Power Dissipation <sup>4</sup>	16.8	10.8	W
$T_{STG}$	Storage Temperature Range	-55 to 175	-55 to 175	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 175	-55 to 175	$^\circ C$

### Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	---	32	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	---	6	$^\circ C/W$

**TM20G03GD**
**N+P-Channel Enhancement Mode Mosfet**
**N-Electrical Characteristics** ( $T_J=25^\circ C$  unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristic</b>						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	30	-	-	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=30V, V_{GS} = 0V,$	-	-	1.0	$\mu A$
$I_{GSS}$	Gate to Body Leakage Current	$V_{DS}=0V, V_{GS} = \pm 20V$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1.2	1.6	2.0	V
$R_{DS(on)}$	Static Drain-Source on-Resistance note3	$V_{GS}=10V, I_D=5A$	-	15	20	$m\Omega$
		$V_{GS}=4.5V, I_D=3A$	-	21	29	
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS}=15V, V_{GS}=0V,$ $f=1.0MHz$	-	490	-	pF
$C_{oss}$	Output Capacitance		-	79	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	61	-	pF
$Q_g$	Total Gate Charge	$V_{DS}=15V, I_D=5.8A,$ $V_{GS}=10V$	-	10	-	nC
$Q_{gs}$	Gate-Source Charge		-	1.7	-	nC
$Q_{gd}$	Gate-Drain("Miller") Charge		-	2.5	-	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-on Delay Time	$V_{DS}=15V, I_D=3A,$ $V_{GS}=10V, R_{REN} = 3\Omega$	-	6	-	ns
$t_r$	Turn-on Rise Time		-	15	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	17	-	ns
$t_f$	Turn-off Fall Time		-	17	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_s$	Maximum Continuous Drain to Source Diode Forward Current	-	-	23	-	A
$I_{SM}$	Maximum Pulsed Drain to Source Diode Forward Current	-	-	30	-	A
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS}=0V, I_s=9A$	-	-	1.2	V
$trr$	Body Diode Reverse Recovery Time	$I_F=5A, dI/dt=100A/\mu s$	-	7	-	ns
$Qrr$	Body Diode Reverse Recovery Charge		-	2	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition :  $T_J=25^\circ C, V_{DD}=15V, V_G=10V, L=0.5mH, Rg=25\Omega, I_{AS}=6A$

3. Pulse Test: Pulse Width $\leq 300\mu s$ , Duty Cycle $\leq 0.5\%$

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**P-Electrical Characteristics** ( $T_J=25^\circ\text{C}$  unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = -250\mu\text{A}$	-30	-	-	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = -30\text{V}, V_{\text{GS}} = 0\text{V}$	-	-	-1	$\mu\text{A}$
Gate-Source Leakage	$I_{\text{GSS}}$	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 20\text{V}$	-	-	$\pm 100$	nA
Gate-Source Threshold voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = -250\mu\text{A}$	-1	-1.5	-2	V
Drain-Source on-State Resistance <sup>3</sup>	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = -10\text{V}, I_D = -4.1\text{A}$	-	33	41	$\text{m}\Omega$
		$V_{\text{GS}} = -4.5\text{V}, I_D = -3\text{A}$	-	42	59	
<b>Dynamic Characteristics<sup>4</sup></b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = -15\text{V}, f = 1.0\text{MHz}$	-	530	-	$\text{pF}$
Output Capacitance	$C_{\text{oss}}$		-	70	-	
Reverse Transfer Capacitance	$C_{\text{rss}}$		-	56	-	
<b>Switching Characteristics<sup>4</sup></b>						
Total Gate Charge	$Q_g$	$V_{\text{GS}} = -10\text{V}, V_{\text{DS}} = -15\text{V}, I_D = -4.1\text{A}$	-	6.8	-	$\text{nC}$
Gate-Source Charge	$Q_{\text{gs}}$		-	1.0	-	
Gate-Drain Charge	$Q_{\text{gd}}$		-	1.4	-	
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{GS}} = -10\text{V}, V_{\text{DS}} = -15\text{V}, R_L = 15\Omega, R_{\text{GEN}} = 2.5\Omega$	-	14	-	$\text{ns}$
Rise Time	$t_r$		-	61	-	
Turn-off Delay time	$t_{\text{d}(\text{off})}$		-	19	-	
Fall Time	$t_f$		-	10	-	
<b>Source-Drain Body Diode Characteristics</b>						
Diode Forward Voltage <sup>3</sup>	$V_{\text{SD}}$	$I_S = -4.1\text{A}, V_{\text{GS}} = 0\text{V}$	-	-	-1.2	V
Continuous Source Current	$I_S$		-	-	-17	A

**Notes:**

1. Repetitive rating, pulse width limited by junction temperature  $T_{J(\text{MAX})}=150^\circ\text{C}$ .
2. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
3. Pulse Test: Pulse width $\leq 300\mu\text{s}$ , duty cycle $\leq 2\%$ .
4. This value is guaranteed by design hence it is not included in the production test.

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### N-Channel Typical Characteristics

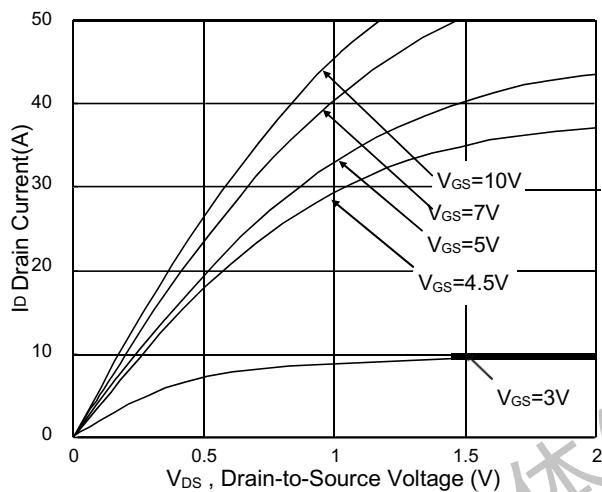


Fig.1 Typical Output Characteristics

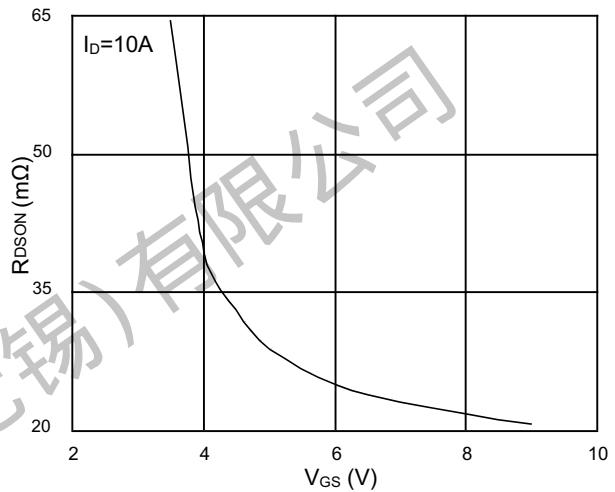


Fig.2 On-Resistance vs. Gate-Source

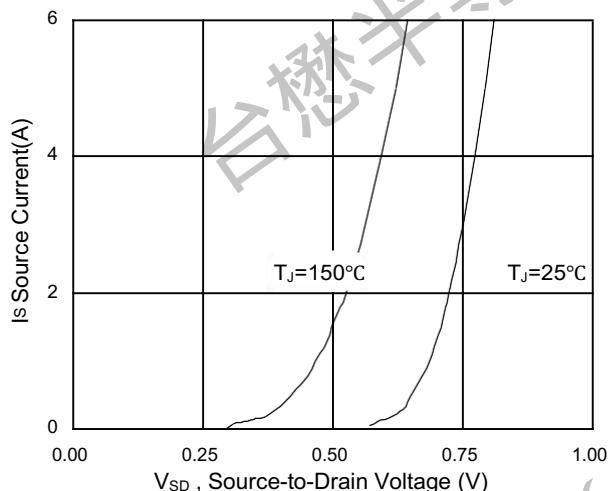


Fig.3 Forward Characteristics Of Reverse

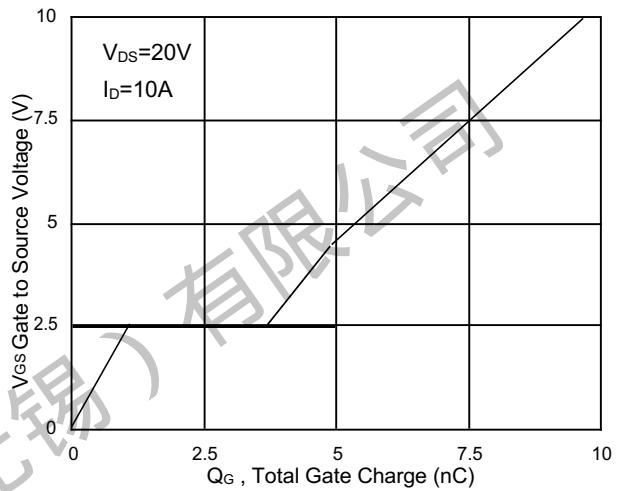


Fig.4 Gate-Charge Characteristics

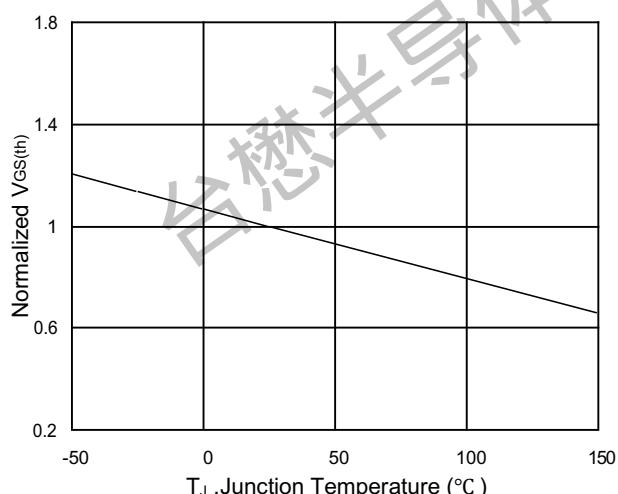


Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$

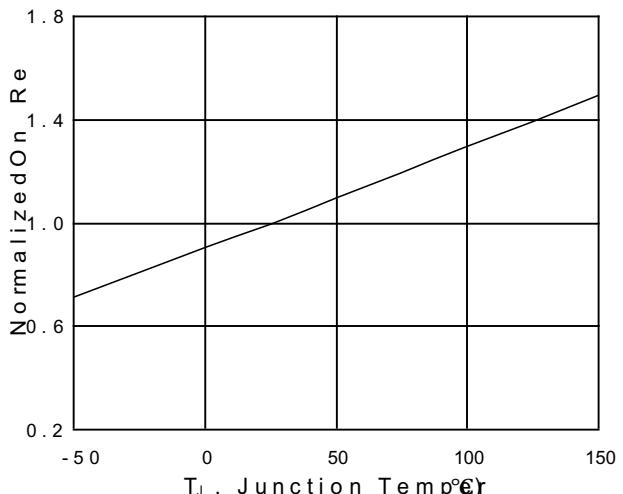


Fig.6 Normalized  $R_{DS(on)}$  vs.  $T_J$

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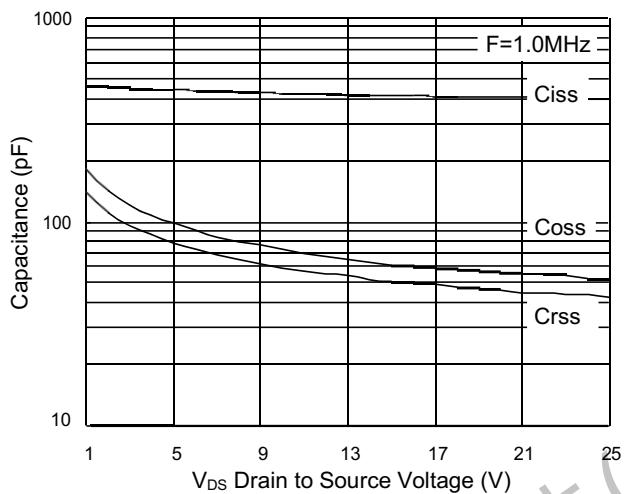


Fig.7 Capacitance

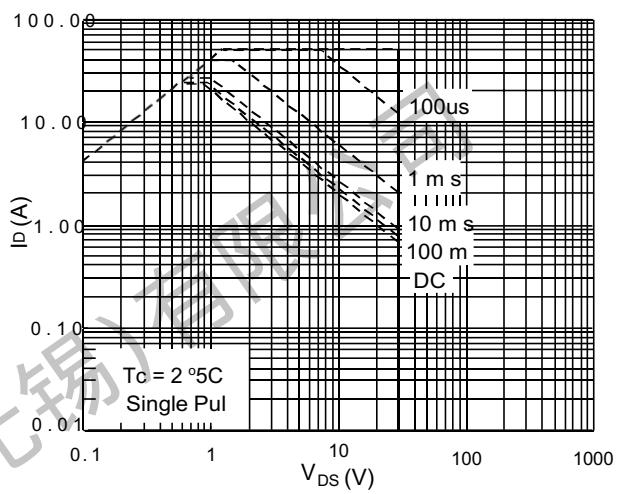


Fig.8 Safe Operating Area

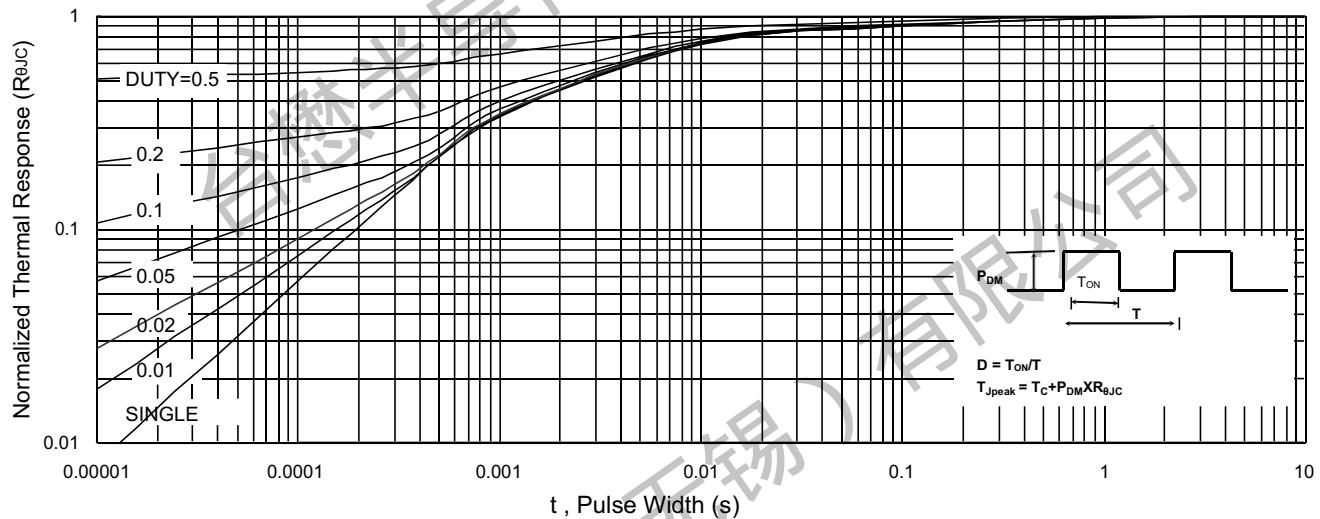
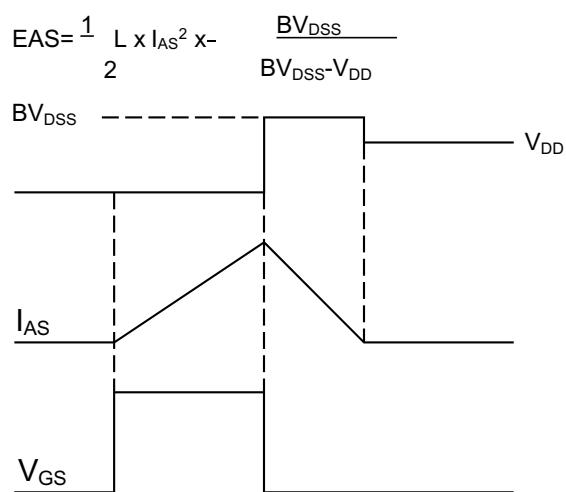
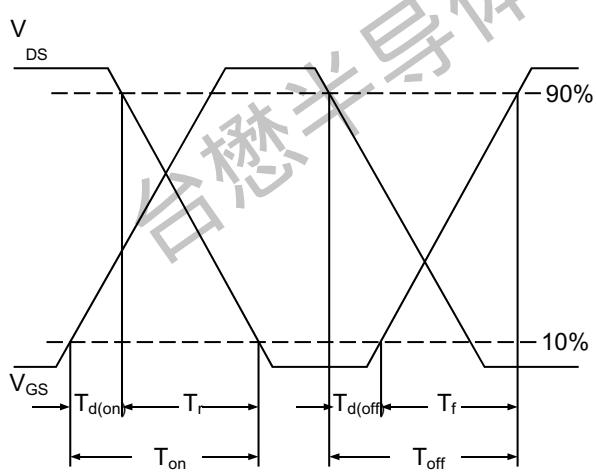


Fig.9 Normalized Maximum Transient Thermal Impedance



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### P-Channel Typical Characteristics

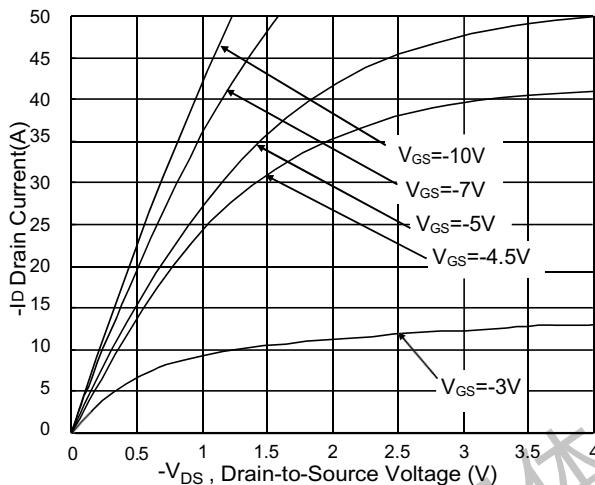


Fig.1 Typical Output Characteristics

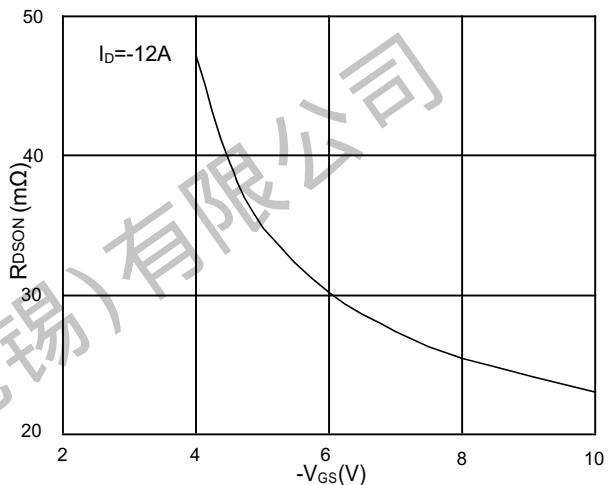


Fig.2 On-Resistance v.s Gate-Source

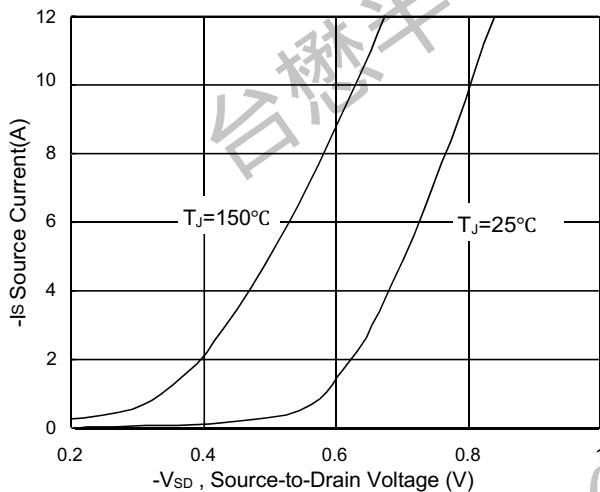


Fig.3 Forward Characteristics Of Reverse

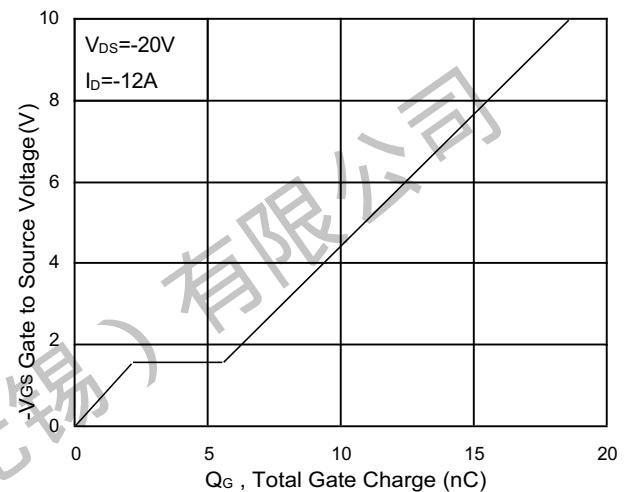


Fig.4 Gate-Charge Characteristics

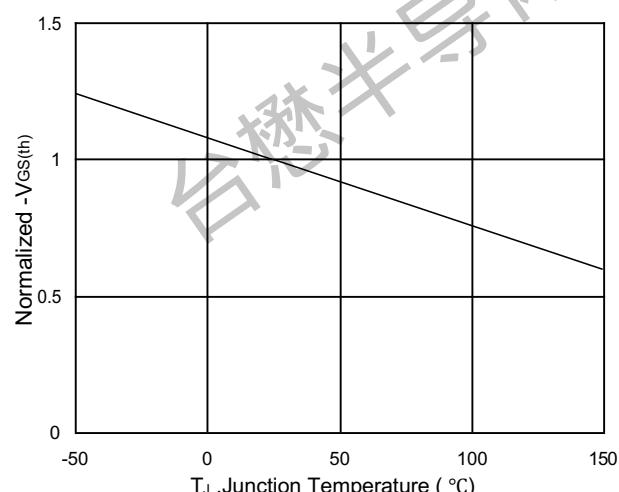


Fig.5 Normalized  $V_{GS(th)}$  v.s  $T_J$

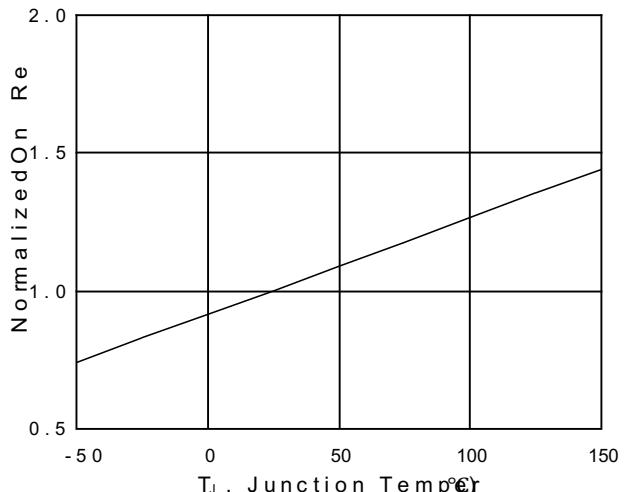


Fig.6 Normalized  $R_{DS(on)}$  v.s  $T_J$

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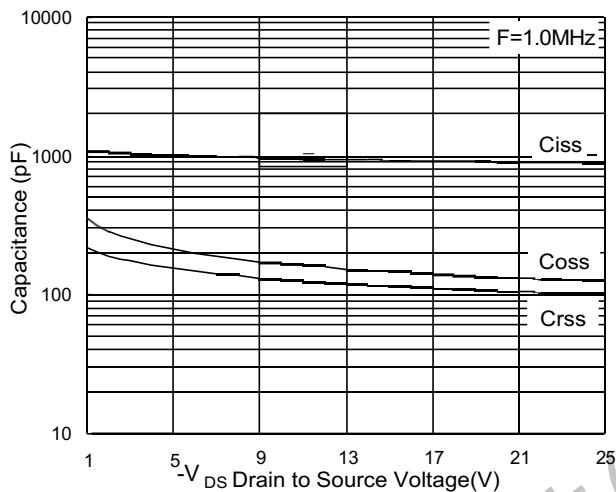


Fig.7 Capacitance

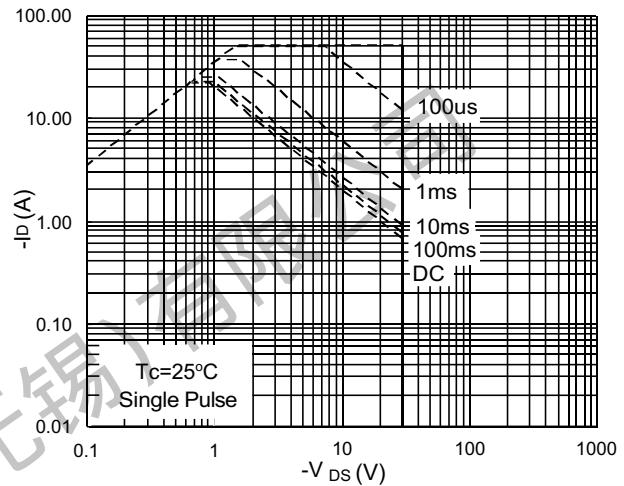


Fig.8 Safe Operating Area

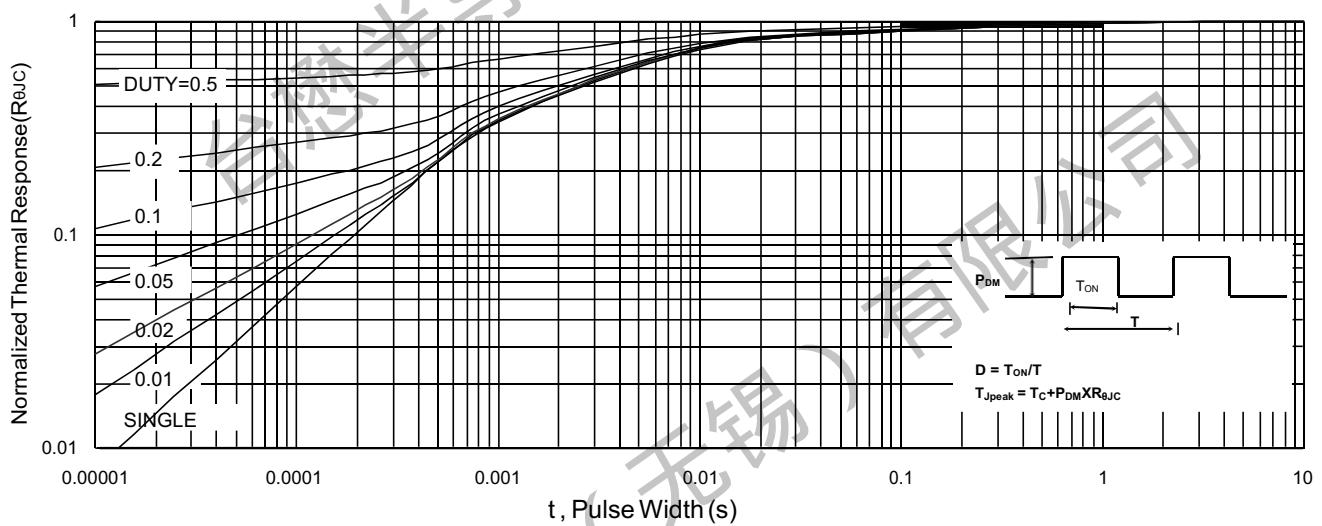


Fig.9 Normalized Maximum Transient Thermal Impedance

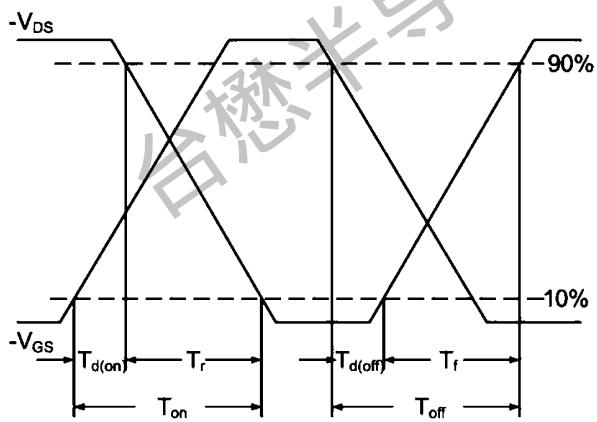


Fig.10 Switching Time Waveform

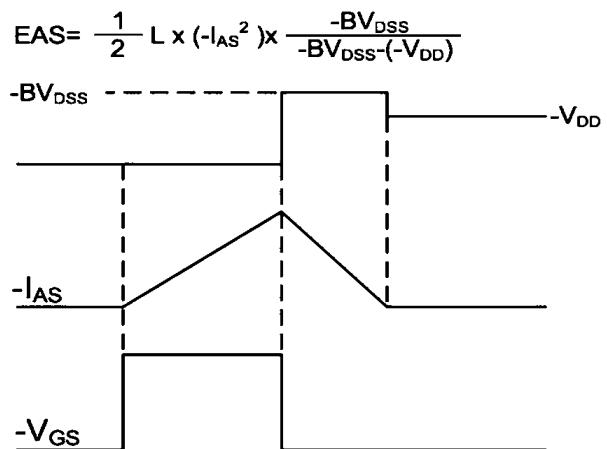
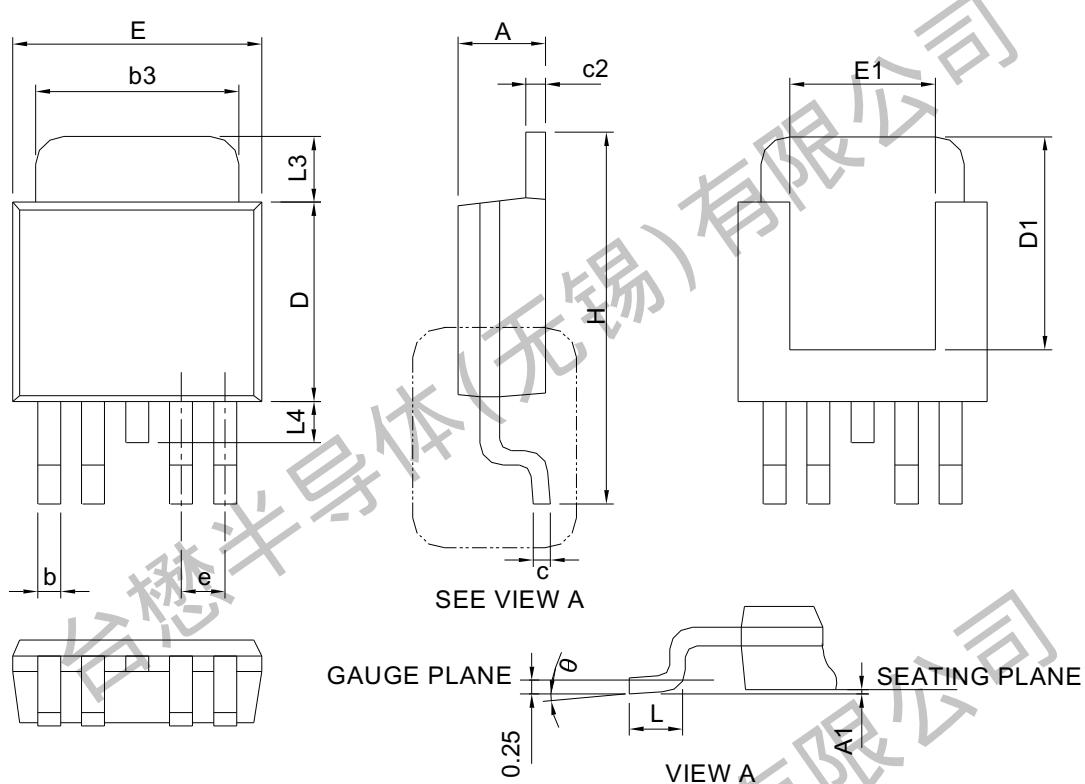


Fig.11 Unclamped Inductive Switching Waveform

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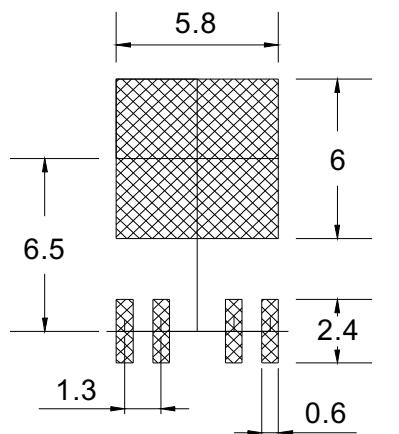
N+P-Channel Enhancement Mode Mosfet

### Package Mechanical Data: TO-252-4L



ITEM	TO-252-4			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	2.18	2.39	0.086	0.094
A1	-	0.2	-	0.008
b	0.50	0.71	0.020	0.028
b3	4.32	5.46	0.170	0.215
c	0.46	0.61	0.018	0.024
c2	0.46	0.89	0.018	0.035
D	5.33	6.22	0.210	0.245
D1	4.57	6.00	0.180	0.236
E	6.35	6.73	0.250	0.265
E1	3.81	6.00	0.150	0.236
e	1.30 BSC		0.051 BSC	
H	9.40	10.41	0.370	0.410
L	1.40	1.78	0.055	0.070
L3	0.89	2.03	0.035	0.080
L4	-	1.02	-	0.040
$\theta$	0°	8°	0°	8°

#### RECOMMENDED LAND PATTERN

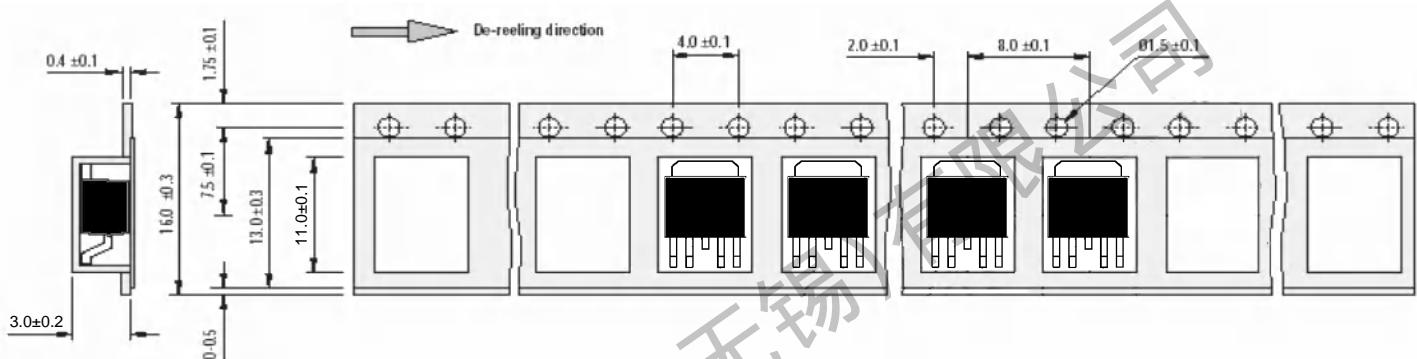


UNIT: mm

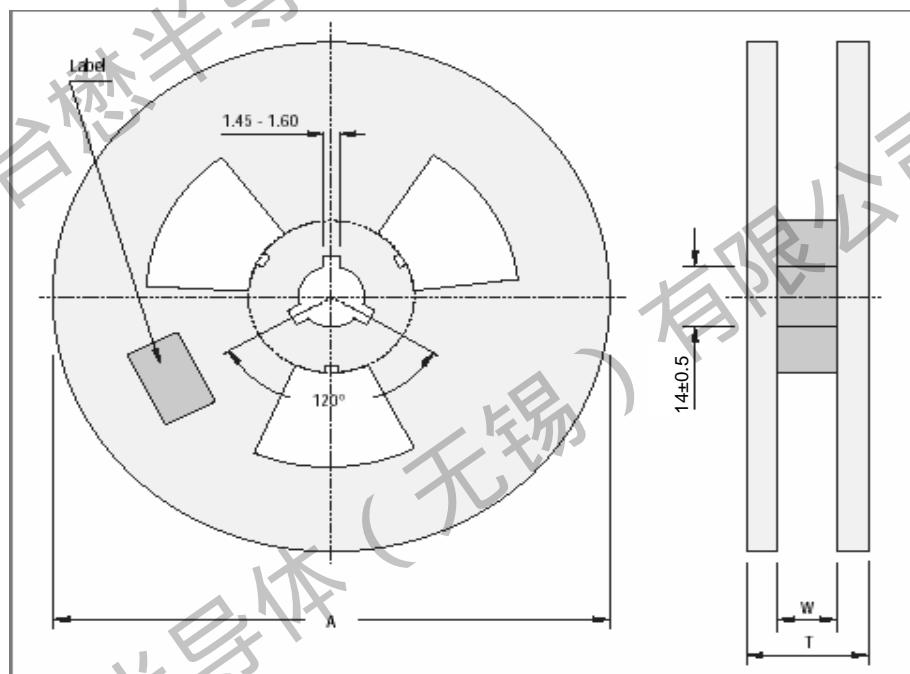
## TM20G03GD

**N+P-Channel Enhancement Mode Mosfet**

TO-252-4L Embossed Carrier Tape



TO-252-4L Reel



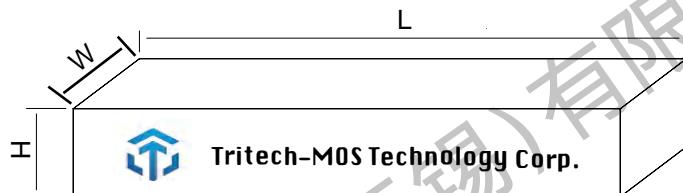
### 1.TO-252-4L Packaging

Package	Packing Form	Quantity		
		Reel	Inner Box	Outbox
TO-252-4L	Reel	2500	5	1

## TM20G03GD

N+P-Channel Enhancement Mode Mosfet

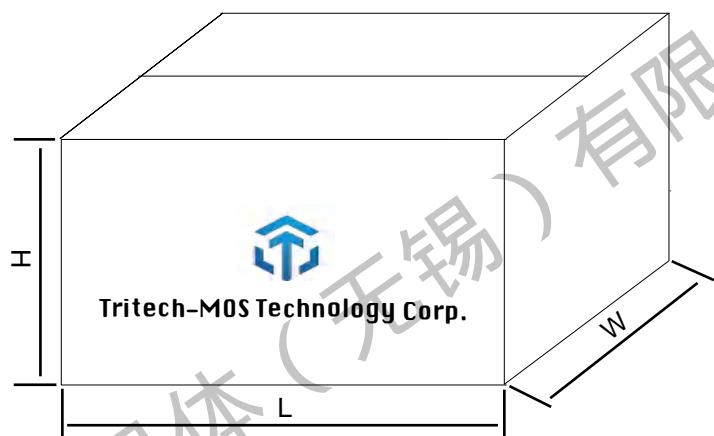
### Inner Box



Dimension : 370 (L)×355(W) ×50(H) mm

Quantity : 2500 × 2Ea = 5000pcs

### Outer Box



Dimension : 380(L)×380(W) ×275(H) mm

Quantity : 5000 × 5Ea = 25000pcs

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#### Revision history:

Date	Rev	Description	Page
2023.06.11	23.06	Original	