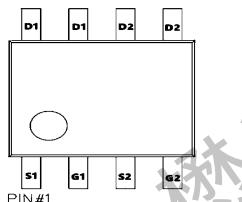
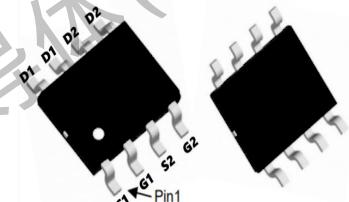
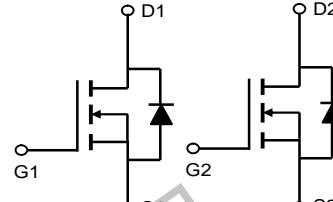


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N+N-Channel Enhancement Mode Mosfet

General Description <ul style="list-style-type: none"> • Low $R_{DS(ON)}$ • RoHS and Halogen-Free Compliant Applications <ul style="list-style-type: none"> • Load switch • PWM 	General Features <p> $V_{DS} = 20V$ $I_D = 8.0A$ $R_{DS(ON)} = 20m\Omega$ (typ.) @ $V_{GS}=4.5V$ 100% UIS Tested 100% R_g Tested </p> 
   <p style="margin-top: 10px;">Marking: 08V02 OR 9926A</p>	

Absolute Maximum Ratings ($T_A = 25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	20	V
V_{GS}	Gate-Source Voltage	± 12	V
$I_D@T_C=25^\circ C$	Continuous Drain Current, $V_{GS} @ 4.5V$	8	A
$I_D@T_C=100^\circ C$	Continuous Drain Current, $V_{GS} @ 4.5V$	5.8	A
I_{DM}	Pulsed Drain Current	29	A
$P_D@T_A=25^\circ C$	Total Power Dissipation	1.65	W
T_{STG}	Storage Temperature Range	-55 to 175	°C
T_J	Operating Junction Temperature Range	-55 to 175	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	---	85	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case	---	36	°C/W

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Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_D=250\mu\text{A}$	20	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=20\text{V}, V_{\text{GS}}=0\text{V},$	-	-	1.0	μA
I_{GSS}	Gate to Body Leakage Current	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=\pm 12\text{V}$	-	-	± 100	nA
On Characteristics						
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_D=250\mu\text{A}$	0.5	0.7	0.9	V
$R_{\text{DS}(\text{on})}$	Static Drain-Source on-Resistance note2	$V_{\text{GS}}=4.5\text{V}, I_D=6\text{A}$	-	20	23	$\text{m}\Omega$
		$V_{\text{GS}}=2.5\text{V}, I_D=5\text{A}$	-	24	29	
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{\text{DS}}=10\text{V}, V_{\text{GS}}=0\text{V},$ $f=1.0\text{MHz}$	-	358	-	pF
C_{oss}	Output Capacitance		-	69.3	-	pF
C_{rss}	Reverse Transfer Capacitance		-	58.5	-	pF
Q_g	Total Gate Charge	$V_{\text{DS}}=10\text{V}, I_D=3\text{A},$ $V_{\text{GS}}=4.5\text{V}$	-	5.6	-	nC
Q_{gs}	Gate-Source Charge		-	0.8	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	1	-	nC
Switching Characteristics						
$t_{\text{d}(\text{on})}$	Turn-on Delay Time	$V_{\text{DS}}=10\text{V},$ $I_D=6\text{A}, R_{\text{GEN}}=3\Omega,$ $V_{\text{GS}}=4.5\text{V}$	-	16	-	ns
t_r	Turn-on Rise Time		-	51	-	ns
$t_{\text{d}(\text{off})}$	Turn-off Delay Time		-	21	-	ns
t_f	Turn-off Fall Time		-	19	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_s	Maximum Continuous Drain to Source Diode Forward Current		-	-	8	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	24	A
V_{SD}	Drain to Source Diode Forward Voltage	$V_{\text{GS}}=0\text{V}, I_s=6\text{A}$	-	-	1.2	V

Typical Performance Characteristics

Figure 1: Output Characteristics

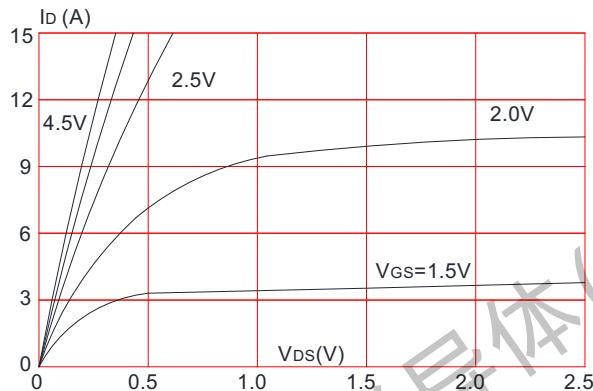


Figure 2: Typical Transfer Characteristics

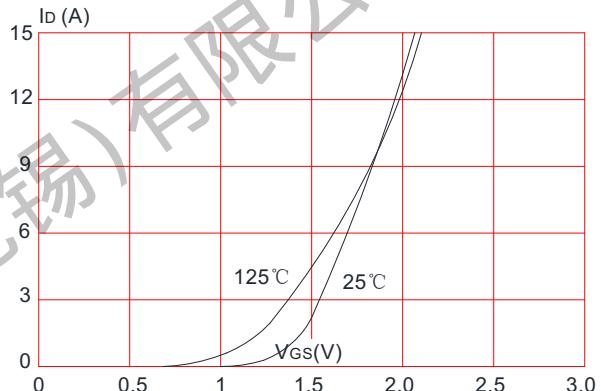


Figure 3: On-resistance vs. Drain Current

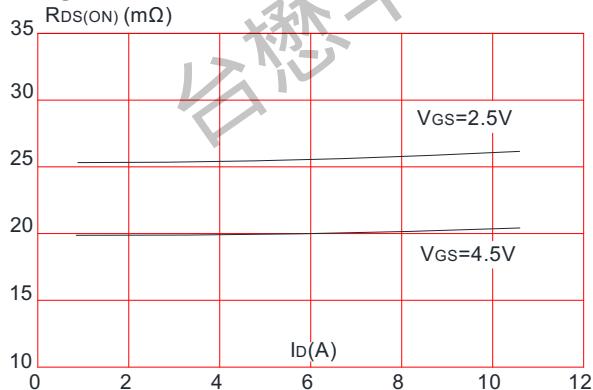


Figure 4: Body Diode Characteristics

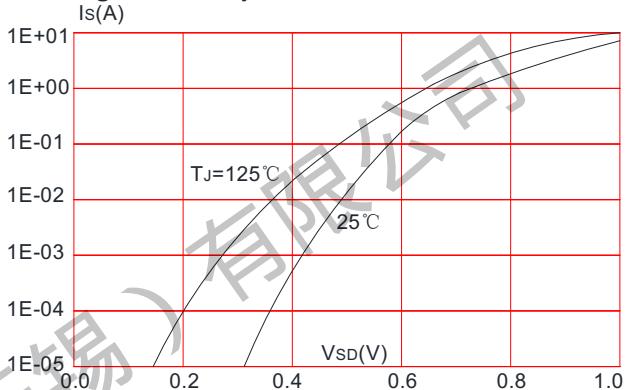


Figure 5: Gate Charge Characteristics

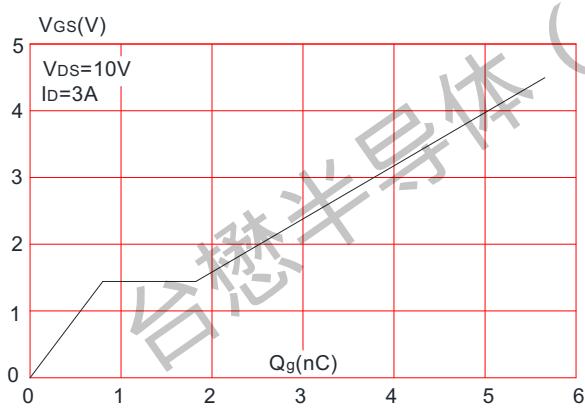
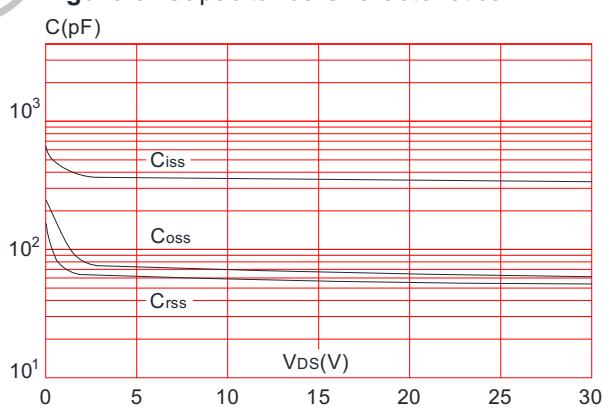


Figure 6: Capacitance Characteristics



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Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

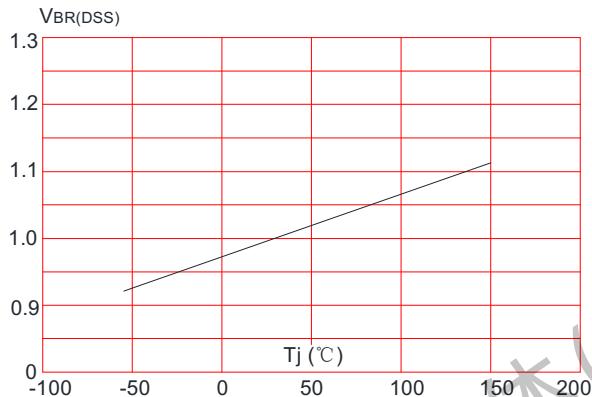


Figure 8: Normalized on Resistance vs. Junction Temperature

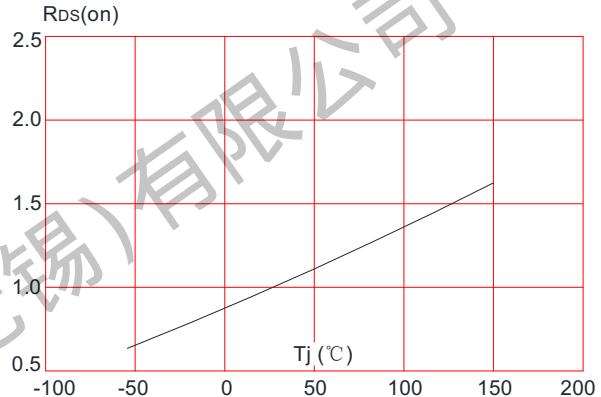


Figure 9: Maximum Safe Operating Area

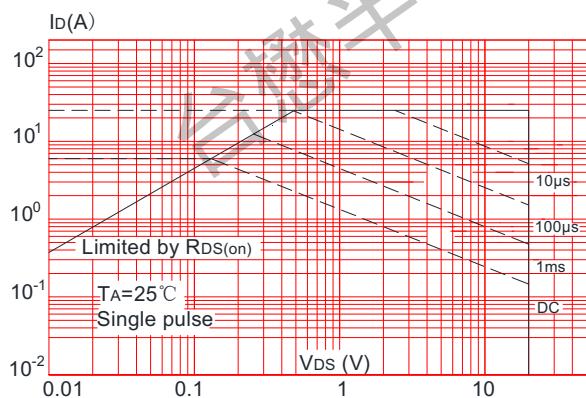


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

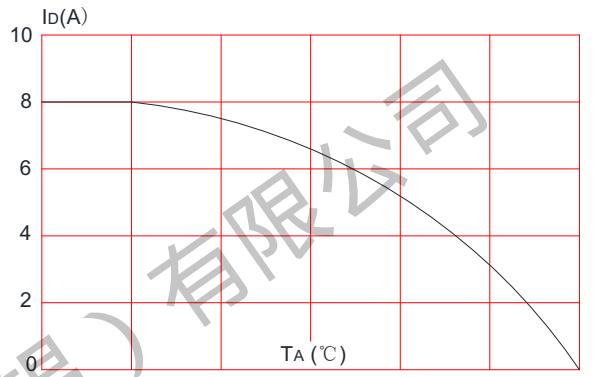
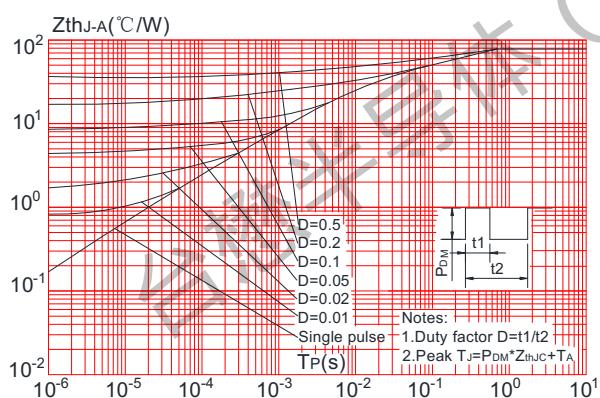


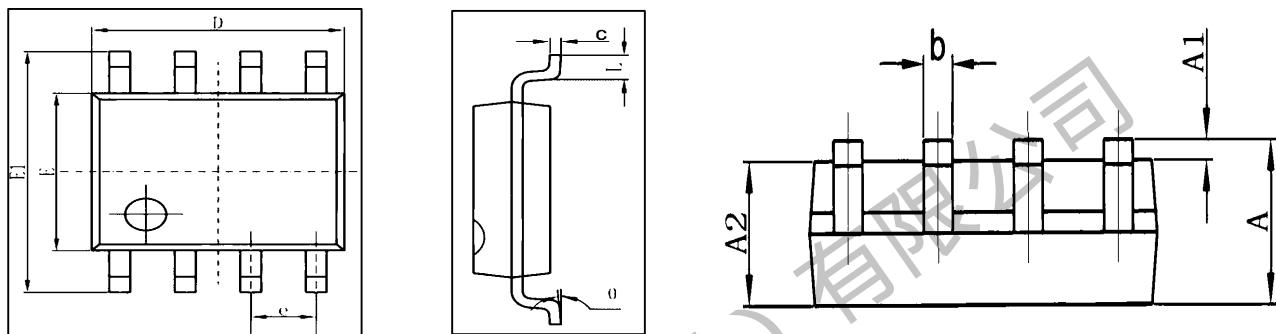
Figure 11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



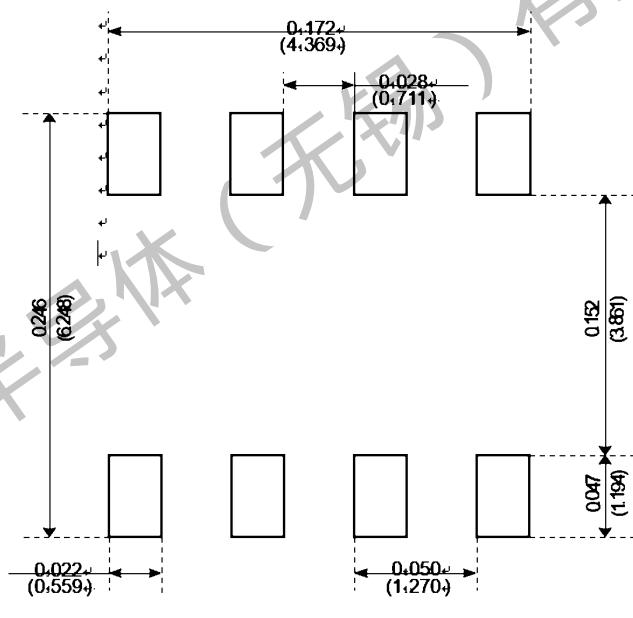
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Package Mechanical Data:SOP-8L



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270 (BSC)		0.050 (BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

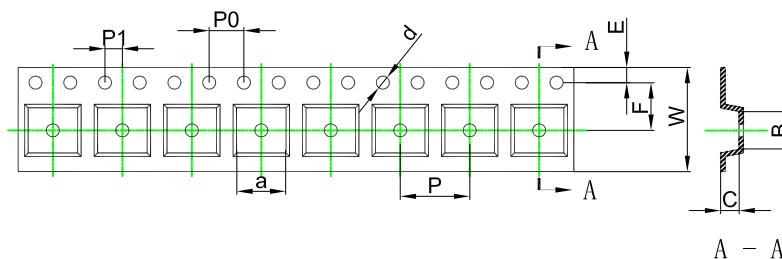


Recommended Minimum Pads

TM08H02S

N+N-Channel Enhancement Mode Mosfet

SOP-8L Embossed Carrier Tape



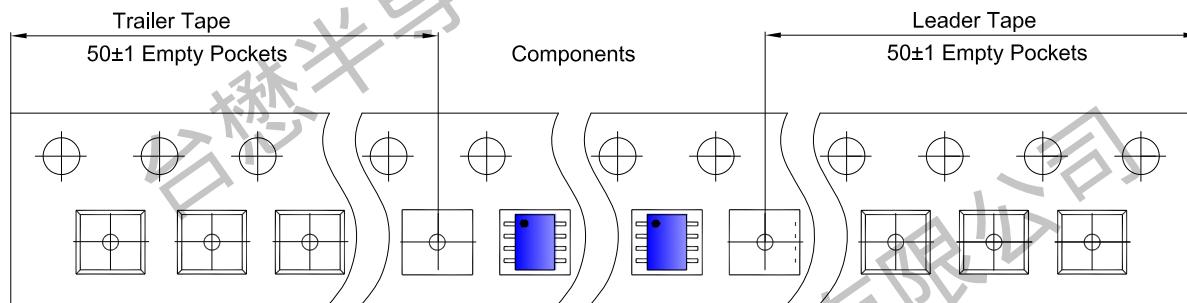
Packaging Description:

SOP-8L parts are shipped in tape. The carrier tape is made from a dissipative (carbon filled) polycarbonate resin. The cover tape is a multilayer film (Heat Activated Adhesive in nature) primarily composed of polyester film, adhesive layer, sealant, and anti-static sprayed agent. These reeled parts in standard option are shipped with 2,500 units per 13" or 33cm diameter reel. The reels are clear in color and is made of polystyrene plastic (anti-static coated).

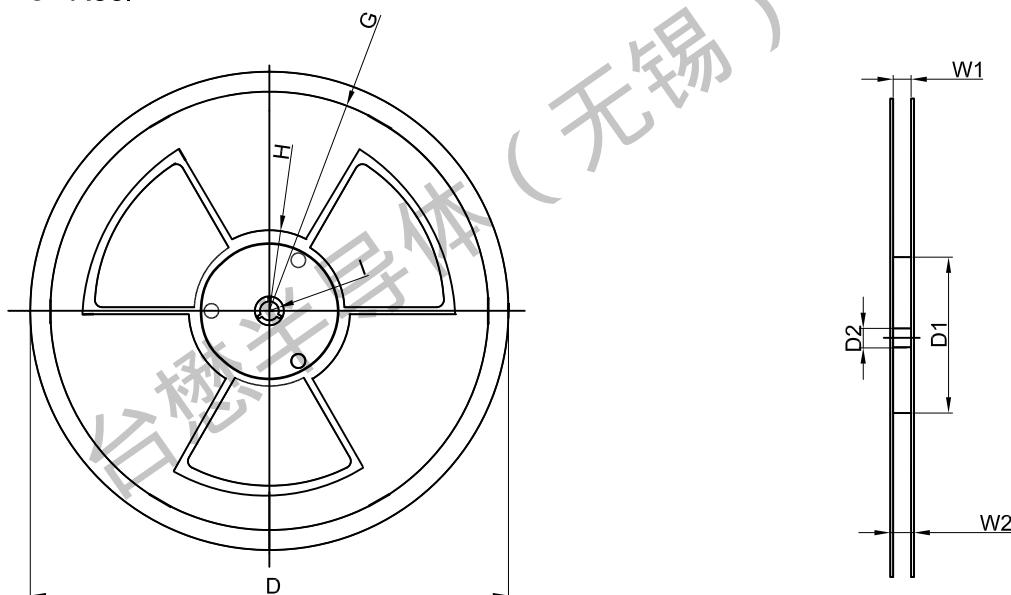
ALL DIM IN mm

Dimensions are in millimeter										
Pkg type	a	B	C	d	E	F	P0	P	P1	W
SOP-8L	6.40	5.40	2.10	Ø1.50	1.75	5.50	4.00	8.00	2.00	12.00

SOP-8L Tape Leader and Trailer



SOP-8L Reel



Dimensions are in millimeter								
Reel Option	D	D1	D2	G	H	I	W1	W2
13"Dia	Ø330.00	100.00	13.00	R135.00	R55.00	R6.50	12.00	14.00

REEL	Reel Size	Box	Box Size(mm)	Carton	Carton Size(mm)	G.W.(kg)
3,000 pcs	13 inch	6,000 pcs	370×355×52	48,000 pcs	400×360×368	

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Revision history:

Date	Rev	Description	Page
2023.03.19	23.03	Original	